RAM-68

8K RAM BOARD

Midwest Scientific Instruments, Inc.
Olathe, Kansas
# PARTS LIST

**6800 8K RAM BOARD**

<table>
<thead>
<tr>
<th>MSI Part #</th>
<th>Quantity</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td><strong>Resistors</strong></td>
</tr>
<tr>
<td>030</td>
<td>9 (R1 - R9)</td>
<td>6.8K</td>
</tr>
<tr>
<td></td>
<td></td>
<td><strong>Capacitors</strong></td>
</tr>
<tr>
<td>178</td>
<td>1 (C1)</td>
<td>25 mfd @ 25 v.</td>
</tr>
<tr>
<td>156</td>
<td>60 (C2 - C61)</td>
<td>.01 mfd</td>
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<tr>
<td></td>
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<td><strong>Semiconductors</strong></td>
</tr>
<tr>
<td>102</td>
<td>13 (D1 - D13)</td>
<td>1N4003</td>
</tr>
<tr>
<td>125</td>
<td>4 (Q1 - Q4)</td>
<td>7805</td>
</tr>
<tr>
<td>419</td>
<td>3 (IC1 - IC3)</td>
<td>8T98</td>
</tr>
<tr>
<td>339</td>
<td>2 (IC4 - IC5)</td>
<td>74S138</td>
</tr>
<tr>
<td>420</td>
<td>2 (IC6 - IC7)</td>
<td>8T97</td>
</tr>
<tr>
<td>201</td>
<td>1 (IC8)</td>
<td>7400</td>
</tr>
<tr>
<td>414</td>
<td>64 (IC9 - IC13)</td>
<td>21L02</td>
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<tr>
<td></td>
<td></td>
<td><strong>Hardware</strong></td>
</tr>
<tr>
<td>967</td>
<td>1</td>
<td>Circuit Board</td>
</tr>
<tr>
<td>844</td>
<td>1</td>
<td>8 - SPST DIP Switch</td>
</tr>
<tr>
<td>1822</td>
<td>4</td>
<td>Heat Sink</td>
</tr>
<tr>
<td>716</td>
<td>4</td>
<td>4-40 x 3/8 BHMS</td>
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<tr>
<td>744</td>
<td>4</td>
<td>#4 ITLW</td>
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<tr>
<td>714</td>
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<td>4-40 Hexnut</td>
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<tr>
<td>1029</td>
<td>5</td>
<td>10 Pin edge socket</td>
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INTRODUCTION

The RAM-68 is an 8K Memory Board which is designed to be compatible with the SS-50 bus structure employed by Southwest Technical Products and MSI 6800 computer systems. The board contains 8,192 eight-bit bytes of fully buffered static memory, having an access time of less than 500 ns. The base address of the board is switch selectable and can be set to begin at any desired 8K increment of memory from 0 to 64K. A convenient DIP switch assembly on the board has eight toggle switches. Only one of the toggle switches may be in the "ON" position at a given time. The remaining seven must be turned off. The table below indicates the appropriate switch setting in order to address the desired memory segment.

<table>
<thead>
<tr>
<th>MEMORY SEGMENT</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
<th>7</th>
<th>8</th>
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<tbody>
<tr>
<td>0000 - 1FFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
</tr>
<tr>
<td>2000 - 3FFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
</tr>
<tr>
<td>4000 - 5FFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>6000 - 7FFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>8000 - 9FFF</td>
<td>OFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>A000 - BFFF</td>
<td>OFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>C000 - DFFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>E000 - FFFF</td>
<td>ON</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
</tbody>
</table>

NOTE: Switch must be mounted as shown in Figure 1 below:

![Figure 1](image-url)
A network of diodes and pull-up resistors is included so that back up Vcc power may be supplied from a battery pack, if desired. This network insures that Vcc remains applied to all memory chips and that the chip select lines are also held in a high state so as to prevent loss of memory during a power failure. Four on-board 7805 regulator chips supply Vcc to the board. The bus must carry an unregulated supply of approximately +8V in order to power the board.

THEORY OF OPERATION

Address lines are brought to the RAM chips through buffer packages IC6 and IC7. These are non-inverting buffers (8T97) which remain permanently enabled at all times. The address lines are active high. Decoder package IC5 (74S138) is used to decode the three highest order address bits along with the valid memory address signal (VMA) and Phase II clock. The output of this decoder is fed to the DIP switch assembly in order to select the desired memory segment. The output of this decoder is, in turn, used to enable IC4 which decodes the lower order address lines in order to select a 1K memory segment on the board. The chip select outputs from decoder IC4 (74S138) are pulled up through a resistor and diode network to the standby Vcc power supply, if used. This network insures that the chip select lines remain high via the battery power supply during power failure.

Data lines are buffered bi-directionally using integrated circuits IC1, IC2, and IC3 (8T98) which are tri-state bus driver packages. Appropriate segments of these packages are enabled depending upon whether a read or write operation is taking place.

The memory chips used in the RAM-68 Board are low-power 2102 static RAMs having an access time of less than 500 ns.

ASSEMBLY

We recommend using a 30 watt soldering iron for all construction. Use only solder with a resin core. Do not use any type of acid based core solders. A 60/40 or 63/37 alloy is recommended.

Care should be taken when soldering semi-conductors and integrated circuits, as overheating of the leads can destroy the device.

All of the necessary hardware and components for assembly is provided. Before beginning construction, check the parts list for any deficiencies.

The use of IC sockets is advisable, especially for mounting the 21L02s. Use a high quality socket such as the Texas Instruments Low Profile.

The PC board is silk screened to show the placement of all major components. The unmarked IC symbols on the board are the locations of the 21L02 memory chips. Refer to the component layout diagram for correct placement and orientation of all parts.
Note that the layout diagram shows more .01 bypass capacitors than shown on the PC board silk screen. These extra bypass capacitors have been included in the kit.

Provisions for the connection of an external back up battery power pack have been made. The point of connection for the battery is located at the upper left-hand corner of the PC board. Refer to the printed sheet for the exact location and polarity markings.

When installing the components on the PC board, be sure to follow the polarity markings of all diodes and electrolytic capacitors. After you have finished construction of the kit, check the board very closely for solder bridges.

NOTE: The DIP switch assembly should be installed in an upright position so that it will correspond with the instructions in the Introduction. See Figure 1.

WARRANTY

All components sold by Midwest Scientific Instruments, Inc. are purchased through normal factory distribution, and any part which fails because of defects in workmanship or material will be replaced at no charge for a period of three months following the date of purchase. The defective part must be returned, postage prepaid, to Midwest Scientific Instruments, Inc. within the warranty period along with a description of the defect.

Sockets may be used on the RAM-68 Memory Board, if desired. However, be sure to use only the highest quality IC sockets available, such as the low profile version available from Texas Instruments. The use of poor quality sockets can create more problems than they solve and may void your warranty on the memory board.

If you have trouble getting your RAM-68 to work properly, it may be returned to the factory for service. Any malfunctioning RAM-68 purchased as a kit, and returned to Midwest Scientific Instruments within the warranty period, which in the judgement of MSI has been assembled with care and not subjected to electrical or mechanical abuse, will be restored to proper operating condition and returned with a minimal charge. Any modules which, in the judgement of MSI, are not covered by the above conditions will be repaired and returned at a cost commensurate with the time and materials required to restore the unit to operating condition. In no case will this charge exceed $100 without prior notification and approval of the owner. Units should be shipped prepaid to MSI along with a letter describing the problem in detail. Be certain to adequately insure the unit as MSI accepts no responsibility for either shipping damages or losses.
INTRODUCTION

The 64K STATIC RAM you have just purchased is one of the best RAM bargains on the SS-50 market today. We have gone to great lengths to combine the right mix of features that are most often required on the high density RAM boards for SS-50 systems.

FEATURES:

STATIC! STATIC! STATIC!

FULLY SS-50C COMPATIBLE

USES THE POPULAR 24-PIN "2716" STYLE 2K X 8 RAM DEVICES

ON BOARD EXTENDED ADDRESSING

BOARD ACCESS TIMES UNDER 250 NS. (WITH 200 NS. RAMs)

PC BOARD IS SOLDER MASKED AND SILK SCREENED

ALL DATA AND ADDRESS LINES FULLY BUFFERED

LOW POWER DISSIPATION (<500 MA. TYP.)

TOP 16K MAY BE DISABLED IN 2K BLOCKS TO ELIMINATE CONFLICTS WITH OTHER MEMORY OR I/O THAT MAY RESIDE IN THESE LOCATIONS

RAMs AND 2716 STYLE EPROMS ARE FULLY INTERCHANGEABLE ON ONE BOARD.

2 MHZ OPERATION IS STANDARD

MAY BE PARTIALLY POPULATED AS A 16K BOARD

BOARD IS CONFIGURED AS 3-16K BLOCKS AND 8-2K BLOCKS (WITHIN ANY 64K BANK) FOR MAXIMUM FLEXIBILITY
PARTS LIST:

7  14 PIN SOCKETS
5  16 PIN SOCKETS
2  20 PIN SOCKETS
32 24 PIN SOCKETS
5 MOLEX 10 PIN CONNECTORS #09-52-3101
1 MOLEX INDEXING PIN #15-04-0219
96 JUMPER PINS OR 32 SHORTING BLOCKS AND 32 3 PIN POSTS
2 HEATSINKS (THM 6106-14) WITH HARDWARE
38 .01-.1 MFD 10V OR GREATER DISK BYPASS CAPS
4 1 MFD 16V OR GREATER TANTALUM CAPACITORS
1 220 OHM 1/4 WATT RESISTOR
5 2.2K TO 5.6K 10 PIN SIP RESISTOR PACKS
2 7805 VOLTAGE REGULATORS
32 TMM 2016, HM6116, OR EGV.
1 74LS00
1 74LS04
1 74LS20
1 74LS30
5 74LS138
2 74LS242
2 74LS244 (U12/U15)
1 74LS266
2 8 POSITION DIP SWITCH
1 RED LED
GENERAL CONSTRUCTION HINTS

For soldering we recommend a 32 watt soldering pencil. Do not use a soldering gun!!! Use small diameter (such as 22 gauge) rosin core 60/40 alloy solder.

Keep the soldering pencil clean with a wet sponge or cloth.

After such components as resistors or capacitors have been soldered, use a small pair of diagonal cutters to remove the excess lead length.

Observe polarities on all tantalum caps and LEDs.

If you notice any discrepancies between the parts received and those listed, please notify us.

LIMITED WARRANTY

Read the enclosed yellow sheet for a statement of our LIMITED WARRANTY as relates to this kit.

Also note that when this product is purchased as a blank board, all that is covered by the limited warranty is the PC board itself.
ASSEMBLY INSTRUCTIONS

[] Give the pc board a good visual inspection for any obvious shorts or opens. There should be none, but a few minutes spent here could save hours later.

[] Using an ohmmeter, insure that there are no shorts between +8 and ground on the buss.

[] Install and solder the 24 pin sockets for IC locations X1 through X32. Note that all pin #1 on all RAMs is oriented to the top.

[] Install and solder the two 20 pin sockets at locations U12 and U15.

[] Install and solder the five 16 pin sockets at locations U4, U9, U10, U13, and U16.

[] Install and solder the seven 14 pin sockets at locations U1, U3, U5, U6, U7, U18, and U19.

[] If your kit contains the jumper pins install and solder the 96 pins into the 3-hole jumper area near the bypass caps at each memory location.

[] If your kit contains the shorting blocks then install the terminal posts in the jumper areas. It may be necessary to cut the posts into groups of three from a strip, use an old pair of diagonal cutters for this.

[] Install and solder two dip switches at locations S1 and S2.

[] Install and solder the five 10 pin SIP resistors at locations U2, U8, U11, U14, and U17.

[] Install and solder the 5 10 pin MOLEX sockets into the holes at the edge of the board.

[] Install the indexing pin into the Molex socket hole directly below C39.

[] Install and solder the bypass caps in locations C5-C42.

[] Install and solder the 220 ohm resistor at location R1.

[] Install and solder the LED at location D51. The cathode (denoted by the flat side) goes toward the right side of the board.

[] Using the heatsinks and hardware install and solder the two 7805 regulators.
[ ] Install and solder the 4 tantal. caps in locations C1-C4. Note the polarity as marked on the board.

[ ] Using any of the regulator mounting tabs as ground, measure the output of each 7805 under power in your system. The output is measured on the right pin of the 7805. The measured voltage should be between 4.75 and 5.25 VDC. Any regulator out of spec must be replaced.

[ ] Install a 74LS00 in socket location U6. All pin #1 are up.
[ ] Install a 74LS04 in socket location U5.
[ ] Install a 74LS20 in socket location U3.
[ ] Install a 74LS30 in socket location U7.
[ ] Install a 74LS138 in socket locations U4, U9, U10, U13, and U16.
[ ] Install a 74LS244 in socket locations U12 and U15.
[ ] Install a 74LS266 in socket location U1.

[ ] Remeasure the voltage regulator outputs on the 7805s now to insure proper operation.

SET UP AND USE

[ ] Determine which locations you need to be RAM and which need to be 2716 EPROMs. Using 24 gauge wire (clipped leads from monolithic caps are usually perfect) or shorting blocks, jumper the center pin at each location to either RAM (right) or ROM (left).

[ ] You must have properly jumpered all locations that you are using to insure that none of the RAMs or EPROMs are damaged.

[ ] Install your RAMs and EPROMs at their predetermined locations. Note that X1 is the first memory location and X32 is the last. Block A includes X1-X7, block B includes X8-X15, block C includes X16-X23, and 0000H is X24, 0800H is X25, etc. Normally a partially populated system is populated starting at location 0000H and continuing until all memory devices are installed.

[ ] Note that the functions for each dip switch are shown next to the switch position.
[] S1 is the multifunction switch. Positions 1 through 4 are the extended addressing switches. Position 5 is used to enable the extended addressing (off is disabled). Position 6 through 8 are enables for the lower 48K of ram where A is the first 16K, B is the second 16K, and C is the third 16K (ON is enabled).

[] S2 is used to enable the top 8 memory locations (16K). Each location is labeled with the memory address that corresponds to the switch position.

[] Unless your system supports the S0 through S3 lines (extended addressing) make sure S1 position 5 is open.

[] By using the A, B, and C switches and switch S2 properly you may populate this board in increments of 16K through 48K and in 2K increments to the end of ram. An ON switch is enabled.