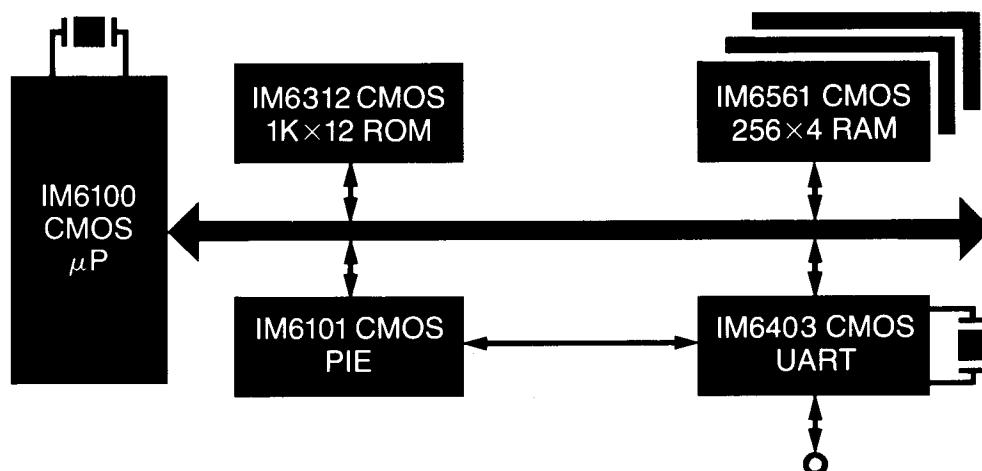


# INTERSiL

## IM6100 CMOS FAMILY SAMPLER



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## INTRODUCTION

An all-CMOS IM6100 based microprocessor system with 256 X 12 random access memory, 1K X 12 read only memory and a serial interface port can be fabricated utilizing seven (7) CMOS LSI devices.

The IM6100 is a single chip, 12-bit microprocessor implemented in silicon gate complementary metal oxide semiconductor (SiG CMOS) technology. It recognizes the instruction set of the Digital Equipment Corporation PDP-8 minicomputer.

The IM6561 is a high speed, low power silicon gate CMOS 1024 bit static RAM organized 256 words by 4 bits. Data-In and Data-Out are multiplexed on the same pins.

The IM6312 is a 1K X 12 mask programmable CMOS ROM with address and data multiplexed on the same pins. The IM6312-001 is 'masked' at the factory with the ODT program (Appendix A). This program allows the user to enter user programs into the RAM via the Teletype and then to edit and execute them.

A Parallel Interface Element, IM6101, provides the universal means of interfacing peripheral devices to the IM6100. It is programmable and configured by the IM6100 for a specific interface during system initialization.

The Universal Asynchronous Receiver/Transmitter, IM6402/03, is a CMOS LSI programmable subsystem for interfacing processors to an asynchronous data channel.

Additional requirements to enable the system to be fully operational are a single 5 volt power supply, two crystals, a 3.3 MHz crystal for the microprocessor and a 3.60 MHz crystal for a 110 baud serial port, control logic to reset and run the system and serial I/O level shifters to communicate with a Teletype or other ASCII terminals (Figure 1).

The dynamic power dissipation of the system (excluding that of the serial I/O level shifters) will be less than 50 mW at 4 MHz and 5 volts.

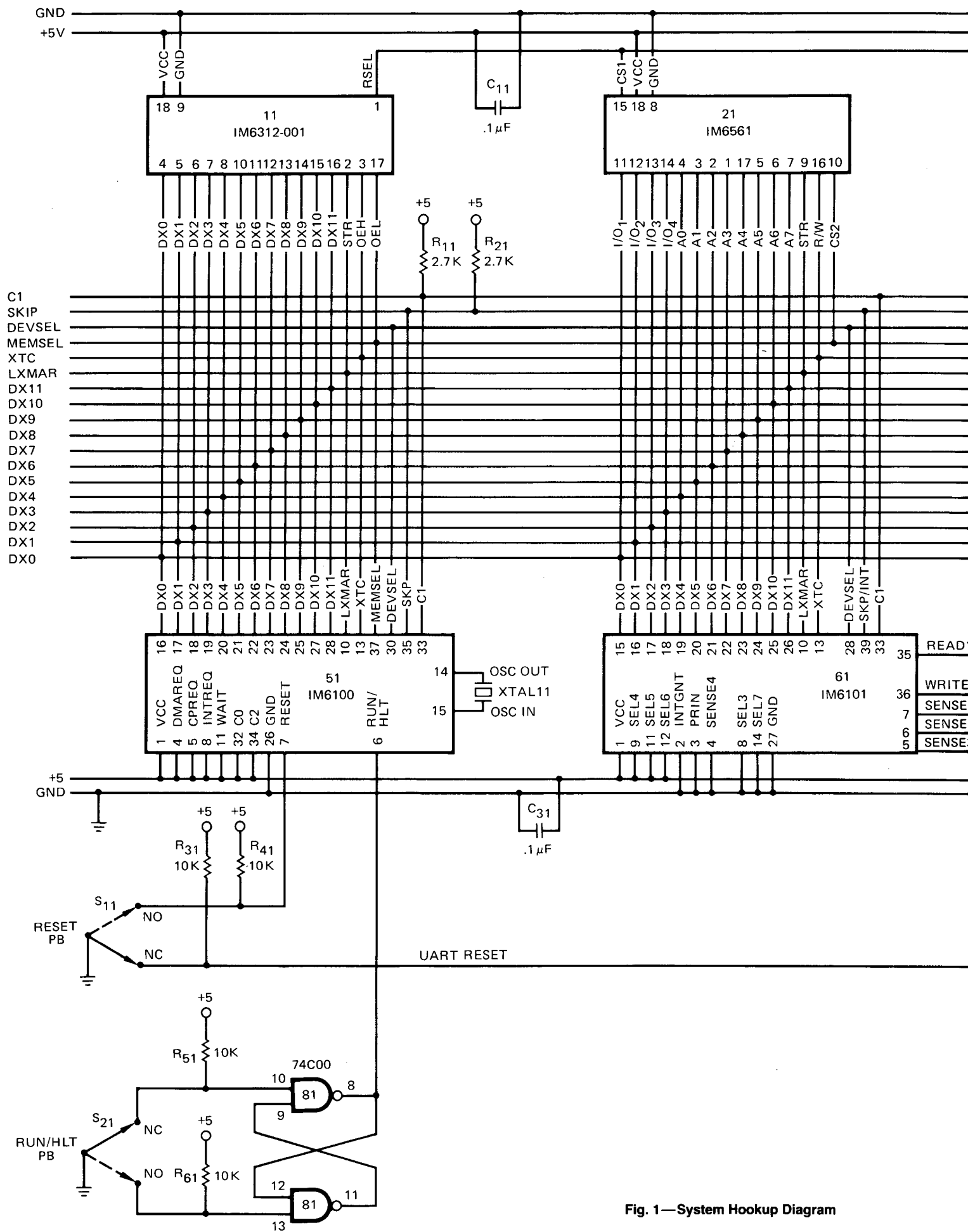


Fig. 1—System Hookup Diagram



## POWER ON PROCEDURE

1. Connect up the CMOS/LSI devices as shown in Figure 1.
2. Choose the level shifting network for the serial I/O device (for example a 20 mA current loop for Teletype) and make the appropriate connections.
3. Turn the power on. Press and release RESET pushbutton. Put the Teletype "ON LINE".
4. Press and release RUN/HLT pushbutton. The ODT program (Appendix A) responds with a CARRIAGE RETURN/LINE FEED and then waits for user commands.

## FUNCTIONAL DESCRIPTION

The IM6312-001 ROM is mask programmed to be enabled when  $DX(0) = DX(1) = 1$  at the falling edge of LXMAR to respond to addresses 6000-7777g.

The RSEL output defines an area in the 4096 word addressing space dedicated to RAM. The IM6312-001 RSEL is programmed to be enabled when  $DX(0) = DX(1) = 0$  at the falling edge of LXMAR to respond to addresses 0000-1777g. If only 256 words of RAM are used,  $DX(2)$  and  $DX(3)$  bits are ignored during addressing. For 1K word RAM systems,  $DX(2)$  and  $DX(3)$  become the high order address bits.

The IM6402/03 UART is hardwired for eight data bits, two stop bits and no parity. If the IM6403 is used, the eleven stage divider chain is selected. This on-chip divider and oscillator allows an inexpensive crystal to be used as a timing source for the IM6403 UART. UARTs require the clocks to be 16X the bit rate (baud). For Teletype operation, it is ten characters per second or 110 baud. Each character consists of eleven bits (one start bit + eight data bits + two stop bits). The UART clock frequency must be 1.76 KHz ( $110 \times 16$ ). If a standard color TV crystal of 3.579545 MHz is used in conjunction with the eleven stage divider chain, one obtains a baud rate of 109.2 ( $3.579545 \times 10^6 \div 211 \div 16$ ) which is well within the tolerance limits for Teletype operation. (The crystal frequency should be 3.6044 MHz for 110 baud.) The same type of crystal can also be used to clock the IM6100. Refer to the section on UART timing for the circuits to be used for the IM6402.

The PIE is wired to respond to the select code 01110 on SEL3-SEL7 and to inhibit priority vectoring. The PIE control registers CRA and CRB are programmed by the ODT to be 0000g and 0060g, respectively. The PIE control signals are then defined as shown below:

WRITE1 and WRITE2	Active Low
SENSE1 and SENSE2	Active on 0 to 1 transition
SENSE3 and SENSE4	Active on 1 to 0 transition
FLAG1, FLAG2, FLAG3 and FLAG4	Initialized Low

Refer to the IM6100, IM6101, IM6312, IM6402/03 and IM6551/61 data sheets for detailed descriptions of the CMOS/LSI devices used in the IM6100 CMOS FAMILY SAMPLER.

The RESET pushbutton is for system initialization. When the IM6100 is reset, it halts with the Program Counter set to 7777g. The RUN/HLT pushbutton is used to alternatively run and halt the CPU. C<sub>1</sub> and SKP lines are pulled up to VCC with 2.7K resistors, since these lines are driven by open drain outputs from the PIE.

The section on level shifting networks details the circuits for 20 mA current loop and RS232 serial I/O interface. User modifications that may be required to select 20 mA current loop and full duplex operation for Teletype are described in Application Bulletin M006 : "Teletype Interface for the IM6100".

#### 20 mA CURRENT LOOP INTERFACE

Figures 2-A, 2-B and 2-C describe the discrete circuits for 20 mA current loops. For reliable Teletype operation, the receive current loop must be established by a voltage source of 15 volts or more.

FLAG1 output of the PIE can be used to control the Reader Relay, if one is installed. Note that the ODT program initializes FLAG1 to be 0 and, therefore, the reader relay to be OFF. The user program must explicitly control the relay by programming FLAG1 output.

The pin connections to a 20 mil 10 pin header are shown.

# 20mA Current Loop Interfaces For Teletype

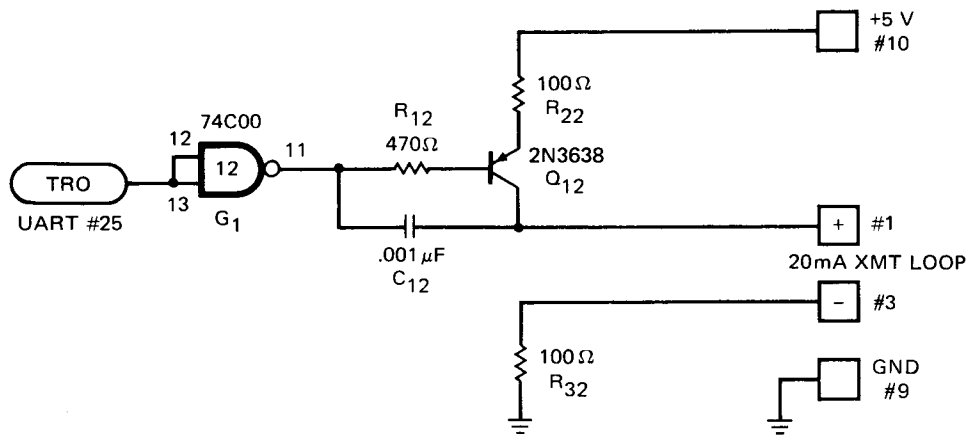


Fig. 2A—XMT Loop

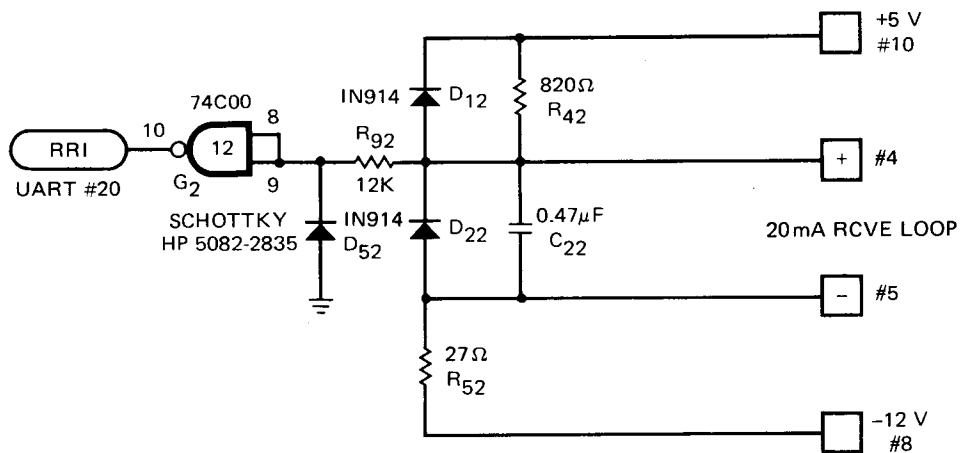


Fig. 2B—RCVE Loop

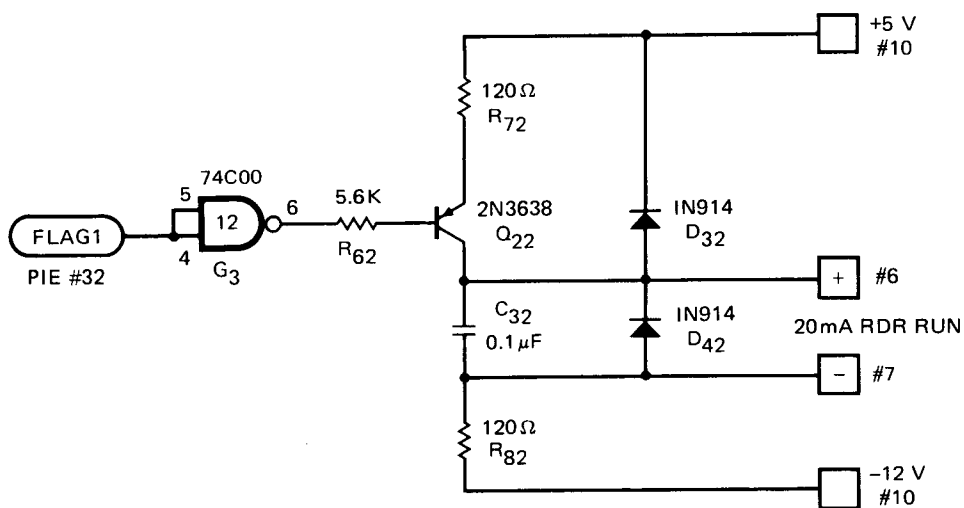


Fig. 2C—RDR RUN Loop

## RS232C INTERFACE

The level shifting networks to conform to the RS232C formats are shown in Figures 3-A and 3-B. The pin connections shown are for an AMP 205858-1 connector as used on the 6960-SAMPLR Board.

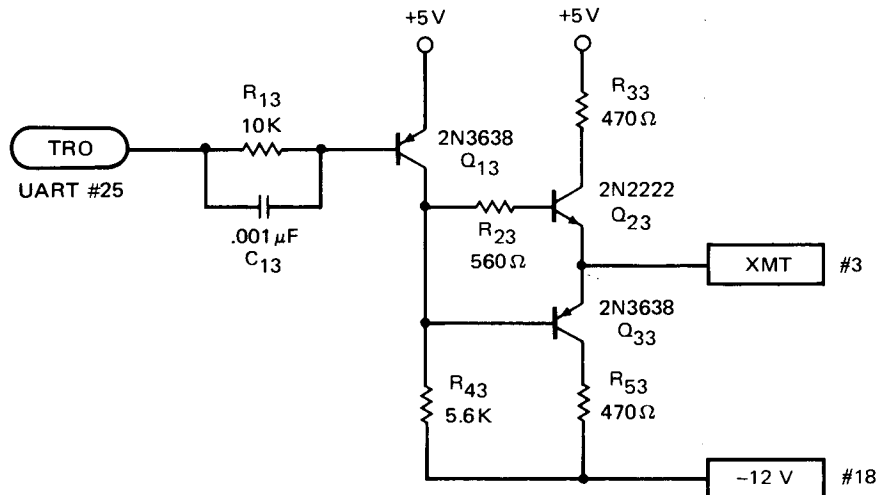


Fig. 3A

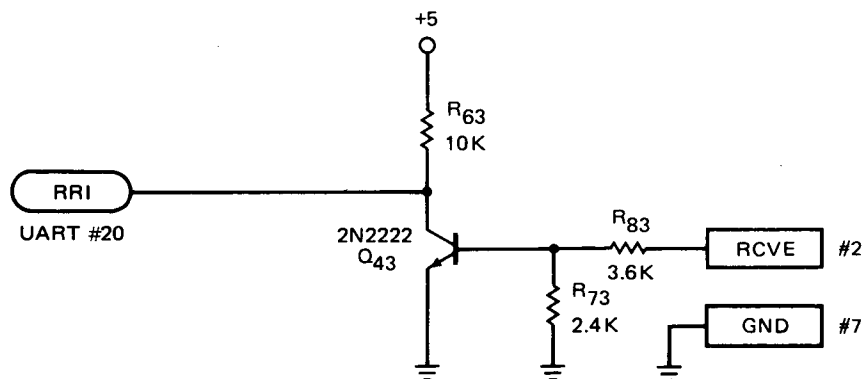


Fig. 3B



## IM6402 UART TIMING

### ICM7213

ICM7213 is a fully integrated oscillator and frequency divider with buffered outputs. The circuit in Figure 4-A divides the crystal frequency by  $2^{11}$  to generate the 16X clocks for the IM6402.

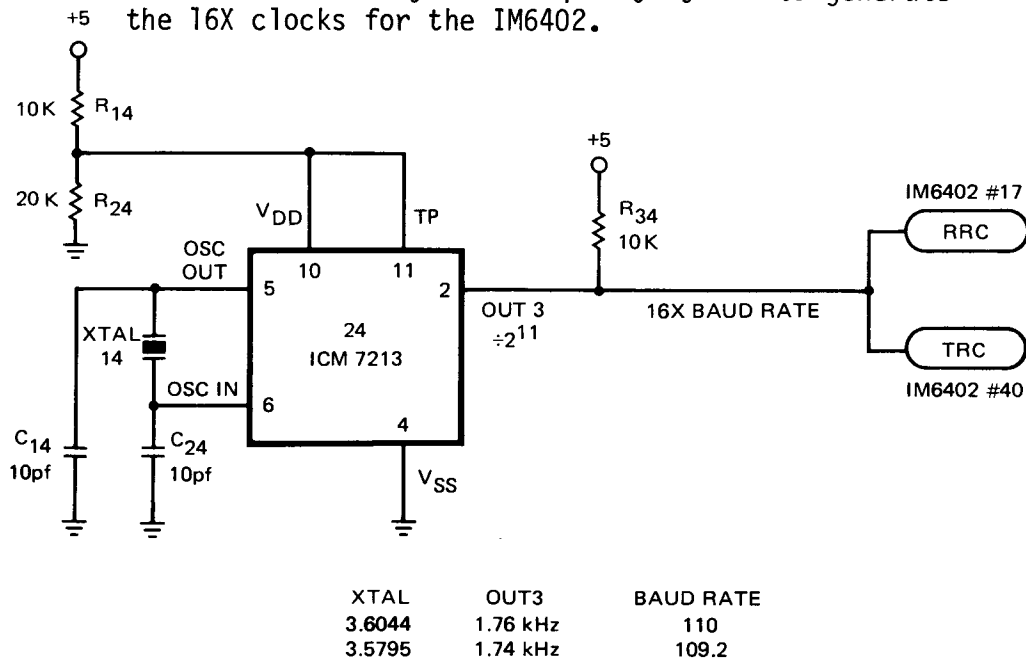


Fig. 4A—IM6402 UART Timing with ICM 7213

### 555 TIMER

The analog timer circuit shown in Figure 4-B can be used for the UART clocks. The resistors and capacitors in the timing circuit must be chosen to be temperature stable.

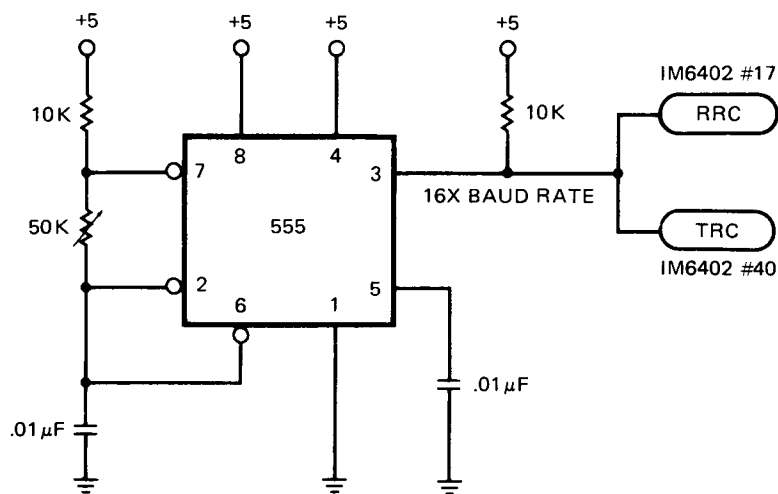


Fig. 4B—IM6402 UART Timing with 555

## BAUD RATE GENERATOR

The 34702 CMOS Baud Rate Generator circuit can be programmed via  $S_0$ ,  $S_1$ ,  $S_2$  and  $S_3$  select lines to generate 13 of the most commonly used baud rates.

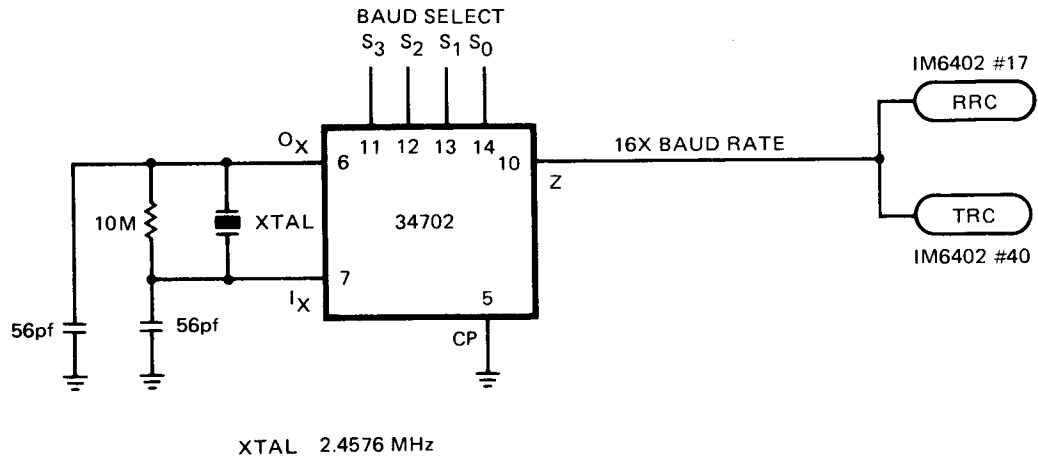
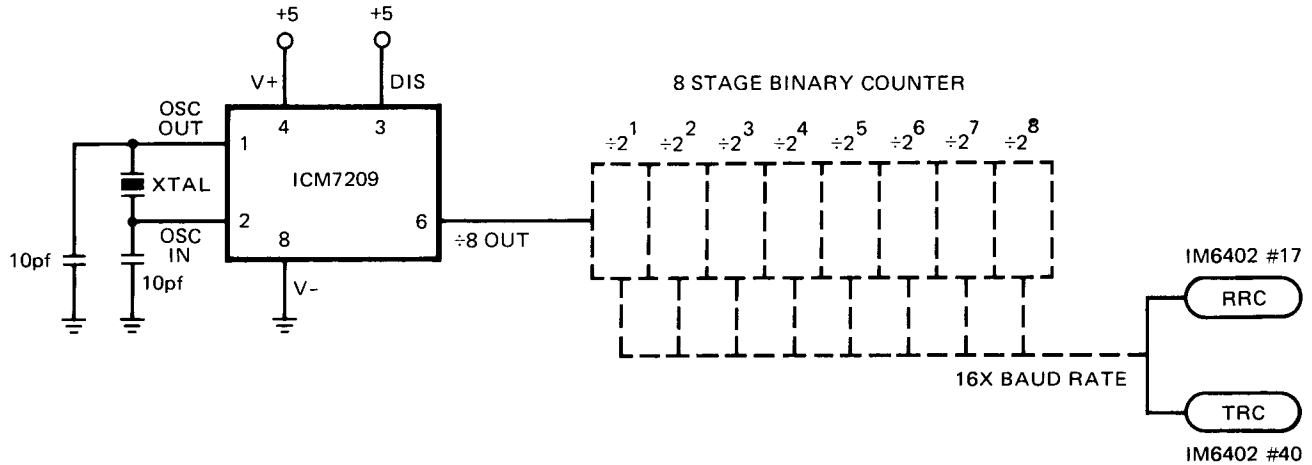


Fig. 4C—IM6402 UART Timing with Baud Rate Generator

XTAL 2.4576 MHz	$S_3$	$S_2$	$S_1$	$S_0$	Baud Rate
	0	0	0	0	
	0	0	0	1	
	0	0	1	0	50
	0	0	1	1	75
	0	1	0	0	134.5
	0	1	0	1	200
	0	1	1	0	600
	0	1	1	1	2400
	1	0	0	0	9600
	1	0	0	1	4800
	1	0	1	0	1800
	1	0	1	1	1200
	1	1	0	0	2400
	1	1	0	1	300
	1	1	1	0	150
	1	1	1	1	110

## ICM7209

An 8-stage divider chain along with the oscillator and 3-stage divider circuit in the ICM7209 with the appropriate XTAL frequency (Figure 4-D) can generate some of the more commonly used baud rates.



XTAL MHz	DIVIDER STAGE	BAUD RATE
3.6044	$2^8$	110
3.5795	$2^8$	109.2
2.4576	$2^6$	300
	$2^5$	600
	$2^4$	1200
	$2^3$	2400
	$2^2$	4800
	$2^1$	9600

Fig. 4D—IM6402 UART Timing with ICM7209

## SINGLE CLOCK

Since the IM6100 family of devices are static, the system may be single clocked. Figure 5 shows the circuit that stops the free running oscillator and introduces manually generated single clocks. ICM7209 is used as a buffered oscillator. The SC EN SW, Single Clock ENable, selects the ICM7209 output or the single clock as the input to the IM6100. The 74C74 is used to ensure integral clocking and to avoid clock slivers while switching from one mode to another. The single clocks are generated by the debounced SC, Single Clock, pushbutton.

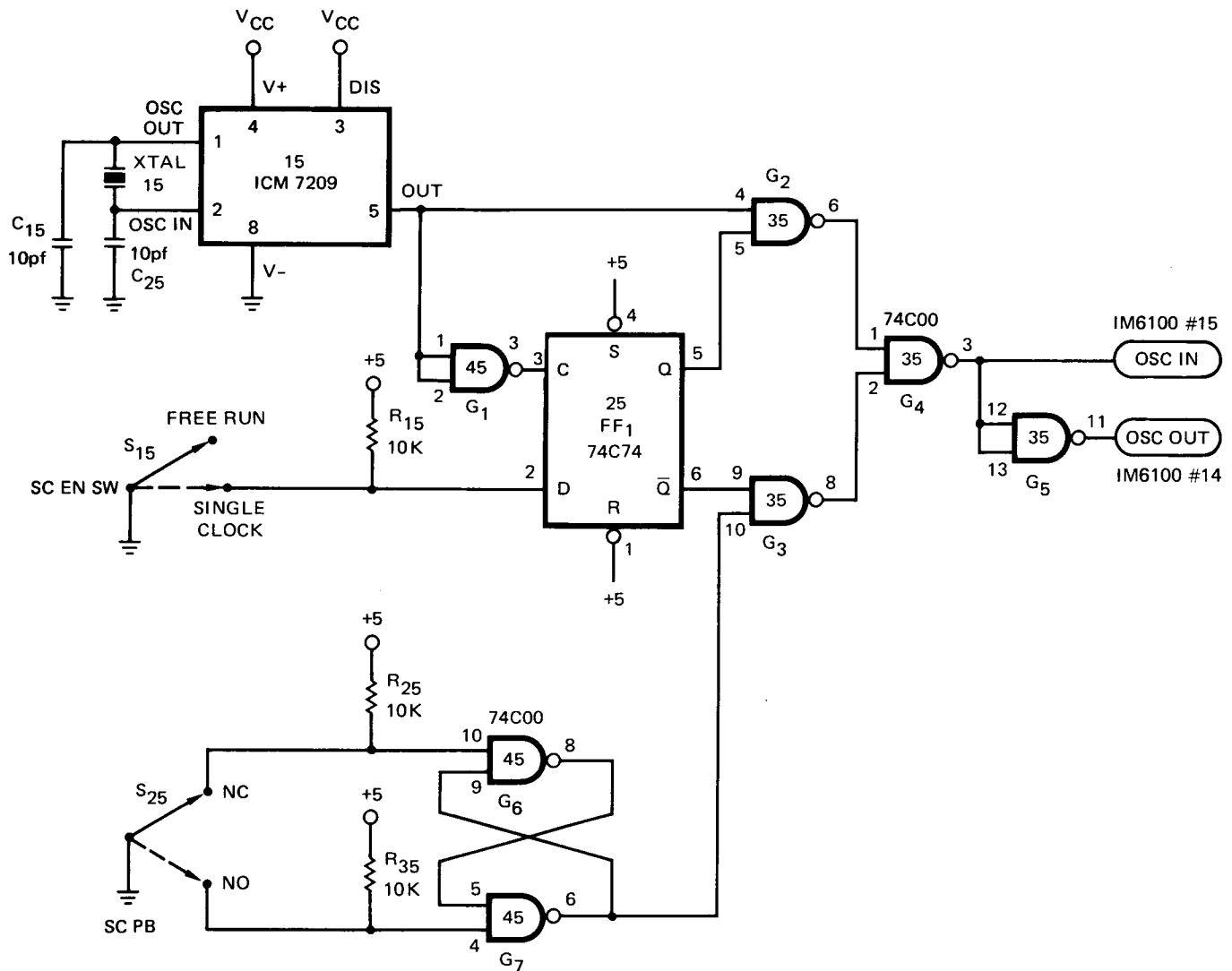


Fig. 5—Single Clock

## SINGLE INSTRUCTION

The IM6100 can be controlled to execute one instruction at a time using the circuit in Figure 6-A. When the SI EN SW, Single Instruction ENable, is in the free run mode, the WAIT signal is high, and the normal operation of the processor is not affected. When the SI EN SW is in the single instruction mode, the WAIT line is pulled down during IFETCH cycle, pausing the IM6100 in the instruction read state. Activating the SI pushbutton will clear the WAIT line, and the processor will go on to execute the instruction and then pause while reading the next instruction and so on.

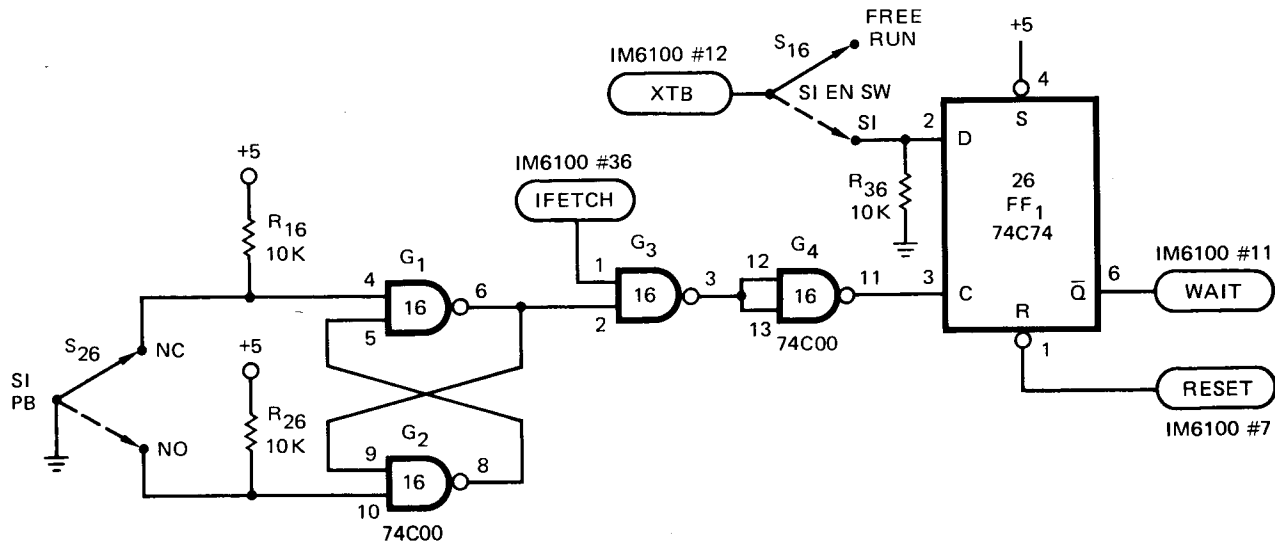


Fig. 6A—Single Instruction

The simple display circuit in Figure 6-B can be used to display instruction addresses, or the state of the DX bus. If the DX EN SW is in the INSTR ADDRESS mode, the quad latches are clocked by IFETCH·LXMAR to display the instruction address. If the DX EN SW is in the DX mode, the DX data flows through the latch.

In the single clock mode, it is useful to have the DX EN SW in the DX state to read the state of the DX bus at every clock.

In the single instruction mode, first set the DX EN SW in the INSTR ADDR mode. The LEDs will then display the address of the instruction about to be executed. Then switch the DX EN SW to the

DX mode, and the display will then show the instruction that is about to be executed since the DX lines contain the instruction as the IM6100 is 'paused' in the read state. Flip the DX EN switch to the INSTR ADDR state before activating the SI push-button for the next instruction and so on to step through instructions, one at a time.

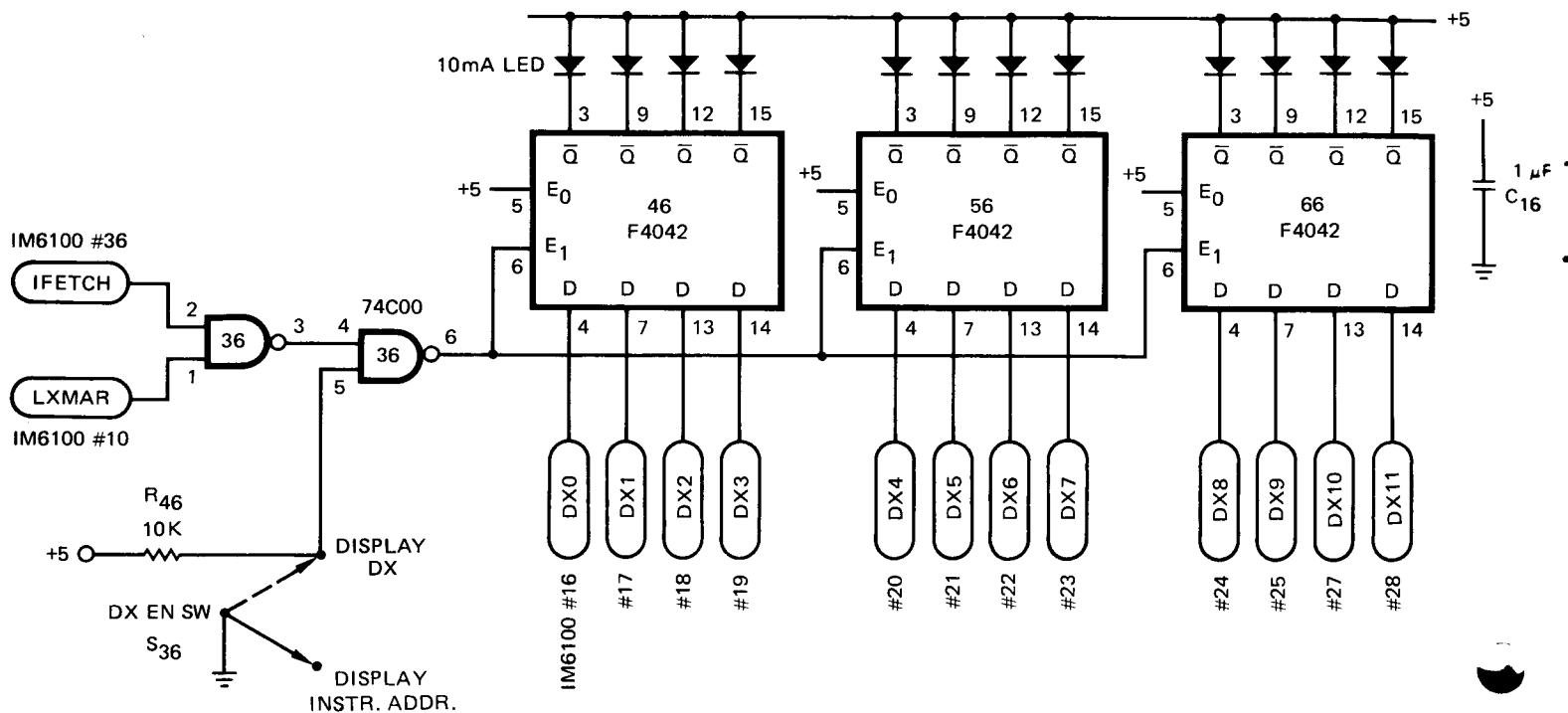


Fig. 6B—Program Counter and DX bus display

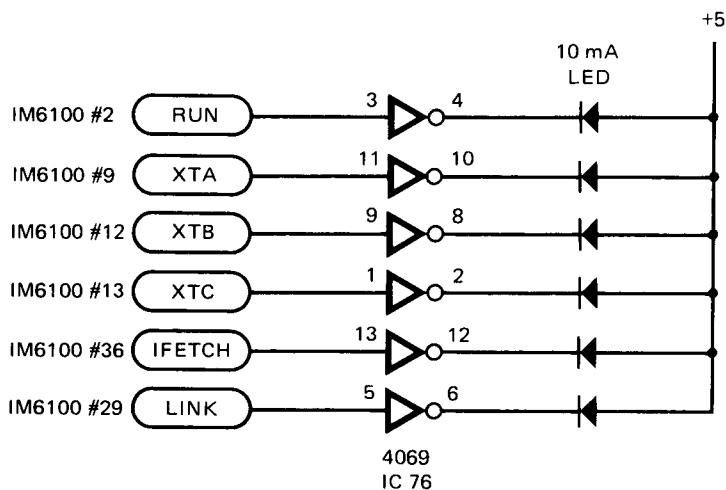


Fig. 6C Link and Timing Signal Display

## PARALLEL DATA I/O

The unused WRITE2 and READ2 control signals of the PIE may be used to implement the parallel data I/O circuit shown in Figure 7.

When the user program executes a READ2 instruction, the 80C95 tri-state buffers enable the data on the DX bus. The DX information can be latched in the 74C174 hex DFFs by executing a WRITE2 instruction. Note that the WRITE2 is programmed to be active low by ODT, and the data is latched by the second edge of the write pulse.

SENSE3-4 and FLAG 1-4 of the PIE are also available to the user.

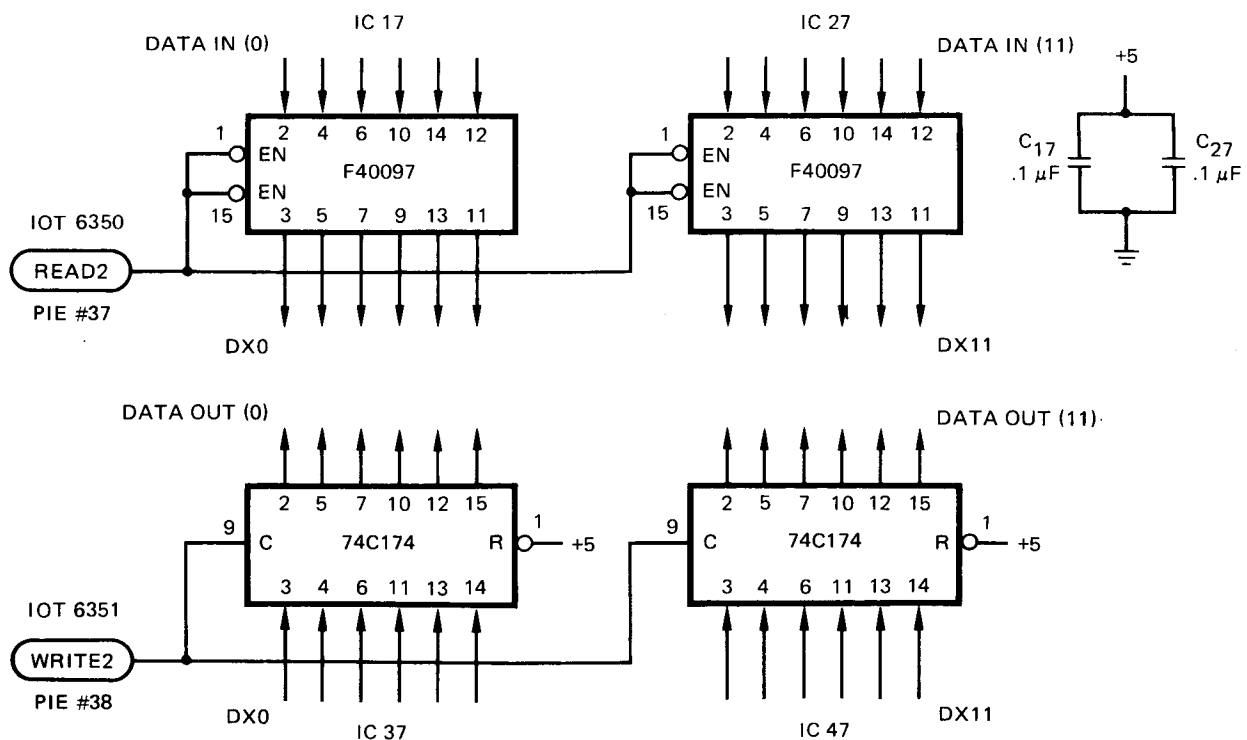


Fig. 7—Parallel Data I/O

## 1. IM6312-001 CMOS OCTAL DEBUGGING TECHNIQUE (ODT) ROM

## INTRODUCTION

This describes the use of the Intersil Octal Debugging Technique (ODT) program.

The commands for Intersil ODT are very similar to those for Digital Equipment Corporation's ODT for use on the PDP-8 in particular, and the DDT family of programs in general.

## RESERVED LOCATIONS

The RAM locations in page zero with octal addresses 5, 6 and 20 through 77 are reserved for use by the ODT program. User programs should not modify these locations.

## COMMANDS

ODT commands consist of a control character or an octal number followed by a control character. All commands are echoed, that is, each character is printed as it is typed in. Octal numbers may consist of octal digits only (no 8's or 9's) and may be from one to four digits long.

Note that ODT does not print a special prompt character (such as an asterisk) before commands may be typed, and does not require that commands be terminated by a special character, such as carriage return (in fact, carriage return is a special command). Commands may be typed in any time the Teletype is idle and are executed as soon as the control character is typed.

## BINARY LOAD COMMAND

L - Load from the tape reader

Typing an L will load BIN tape (ignoring change field characters) from a reader. To use command, type L after a prompt, place tape in reader on the leader-trailer part, then start the tape reader. The BIN tape will be read into the current field and the checksum will be printed out on the Teletype following the end of the load. This should be 0000 for a proper load

## EXAMINE/MODIFY COMMANDS

/ (slash) - opens a location

Typing an octal number nnnn followed by a slash causes the location whose address is nnnn to be opened. When a location is opened, its content is printed out as an octal number. Typing a slash not preceded by a number causes the most recently opened location to be reopened.



(carriage return) - closes a location

When a location is open, typing an octal number, nnnn, followed by a carriage return causes the contents of the location to be changed to the number nnnn and closes the location. Once a location is closed, its contents cannot be changed without re-opening the location. Typing a carriage return not preceded by a number causes the location to be closed without modifying its contents. If a carriage return (possibly preceded by a number) is typed when no location is open, it has no effect.

(line feed) - closes and opens next

When a location is open, typing a line feed causes the location to be closed and the next memory location (that with an address one higher than the current location) to be opened. The address of the new location will be typed out, followed by a slash, followed by the contents of the new location. The effect is the same as if the user had typed a carriage return, followed by typing the address of the next location and the slash. Typing an octal number, nnnn, before typing the line feed causes the contents of the old location to be changed to nnnn. Then the old location is closed and the next location is opened, as described above.

← (back arrow) - closes location and opens indirect reference

When a location is open, typing a back arrow causes the location to be closed. The contents of the location are then treated as an indirect reference. That is, the content of the old location is taken as an address, and the new location is opened. As with the line feed command, the address of the newly opened location is typed, followed by a slash, and the effect is the same as if the user had typed the address and slash. If while a location is open, an octal number, nnnn, is typed followed by a back arrow, the content of the open location is changed to nnnn and proceeds as above. Note in this case that the address of the new location opened will be the same as the number just typed in. On most Teletypes, the back arrow character is the same as shift 0.

↑ (up arrow) - closes location and opens memory reference  
 This command behaves identically to the back arrow command except that the contents of the location are treated as a memory reference instruction, and it is the location referenced by that instruction that is opened. The location opened is that immediately referenced by the instruction. If the instruction is indirect (bit 3 is set to 1), then typing the up arrow only opens the location containing the pointer to the operand of the instruction. To open the effective location referred to by an indirect instruction, type an up arrow (memory reference) followed by a back arrow (indirection). On most Teletypes, the up arrow character is the same as shift N.

### Example

Say that the simple program

```

200 7200 CLA
201 1604 TAD I X
202 2204 ISZ X
203 7402 HLT
204 0205 X, Y
205 0000 Y, 0

```

is stored in memory. Then the following might be the result of a session with ODT. (Note: The underlined portion is typed by the user, and the remainder is typed by the computer. The symbol CR stands for carriage return, and LF stands for line feed.)

200/7200 <u>LF</u>	list the program in octal
0201 /1604 <u>LF</u>	
0202 /2204 <u>LF</u>	
0203 /7402 <u>LF</u>	
0204 /0205 <u>LF</u>	
0205 /0000 <u>CR</u>	
<u>200/7200 7201</u> <u>CR</u>	change CLA to CLA IAC
<u>77201</u> <u>LF</u>	verify the change
0201 /1604 <u>↑</u>	find the location referenced by TAD I X
0204 /0205 <u>←</u>	
0205 /0000 <u>CR</u>	
<u>202/2204 2205</u> <u>LF</u>	change ISZ X to ISZ Y
0203 /7402	

## PROGRAM CONTROL AND BREAKPOINT COMMANDS

### G - go to

Typing an octal number, nnnn, followed by a G causes ODT to begin executing the program stored in memory, starting at location nnnn.

### B - breakpoint

A breakpoint should be set at some location that will be executed by the user's program, upon the execution of which he wishes control to return to ODT. Typing an octal number, nnnn, followed by a B causes ODT to set a breakpoint at location nnnn. Only one breakpoint can be set at a time, therefore, setting a breakpoint clears the previous breakpoint. Typing a B without preceding it by a number causes the current breakpoint to be cleared. When execution of a program with a breakpoint resumes (via the G or C commands) the content of the breakpoint location is replaced with a special trap instruction that returns control to ODT. When control returns to ODT, the trap instruction is replaced by the original contents of the location. Then ODT prints out the location at which the trap occurred followed by a left parenthesis followed by the contents of the accumulator when the trap occurred. ODT then waits for the user to type in a new command.

### C - continue

After a breakpoint causes control to return to ODT from a user program, typing C causes the program to resume execution where it left off. Execution will resume from the location that the program left off, even if the breakpoint was reset to a new location. If an octal number, nnnn, is typed before the C, then after the resumed program has executed the breakpoint once it will continue execution (rather than returning control to ODT) until the breakpoint has been executed nnnn more times; then control returns to ODT just as it does after a normal breakpoint.

### A - examine/modify accumulator, link, MQ

Three consecutive ODT RAM locations are reserved for storing the contents of the AC, link and MQ registers when a breakpoint occurs. When execution of the user's program resumes (via the G or C command), the contents of these registers are restored from these locations. Typing A causes the first of these locations, containing the contents of the AC, to be opened. Following this by a line feed opens the next location, containing the contents of the link (either a one or a zero). Following this by a line feed opens the last location, containing the contents of the MQ, to be opened. All of the commands of the previous section (/,

↑, ←, carriage return, line feed) can be used to examine and modify these registers. If one of the registers, AC, is changed during the breakpoint, then the AC will have the new value when program execution resumes. This can be used to change the contents of the AC. The same facility can also be used to set the initial contents of the registers before the first G command begins the program's execution.

### Example

Say that the simple program

300	7001	START, IAC
301	7440	SZA
302	5300	JMP START
303	7402	HLT

is stored in memory. Then the following might be the result of a session with ODT.

<u>A1764</u> <u>OLF</u>	accumulator contains garbage, clear it
<u>0050</u> / <u>0001</u> <u>OCR</u>	same for link
<u>302B</u>	set breakpoint at JMP START
<u>300G</u>	execute program
<u>0302</u> (0001	breakpoint occurs; accumulator has been incremented
<u>7774C</u>	Go past breakpoint 1 + 7774 times
<u>0302</u> (7776	breakpoint occurs
<u>303B</u>	reset breakpoint to HLT instruction
<u>C</u>	
<u>0303</u> (0000	program stops when AC reaches 0 again
<u>A000OLF</u>	
<u>0050</u> / <u>0001</u> <u>CR</u>	link has been changed by overflow
<u>B</u>	clear all breakpoints

### WORD SEARCH COMMANDS

#### M - open search mask, lower bound, upper bound

The mask, lower bound and upper bound for word searches are kept in that order in three consecutive reserved ODT locations. The first of these locations, the mask, can be opened by typing M. This will cause the current value of the mask to be printed out. The user can enter a new value by typing it followed by any of the word modifying commands (carriage return, line feed, up arrow or back arrow). Typing a line feed causes the next location, containing the lower search bound, to be opened. Its contents will be printed out and it can be modified in the same way. Typing a line feed again causes the final location, containing

the upper search bound, to be opened. Initially, the value of all three locations is unspecified. The user should set these locations to the desired value before giving the first W command.

#### W - word search command

Typing an octal number, nnnn, followed by a W causes a word search to occur. The search proceeds as follows: The number, nnnn, that was typed is masked and remembered as the quantity which is being searched for. (The operation of masking is to take the bitwise boolean AND of the given word with the contents of the mask word.) Then each location, beginning with the location whose address is stored in the lower bound word, is masked and compared with the quantity being searched for. If the two are equal, then the address of the word, followed by a slash and the (unmasked) contents of the word are printed out. Then the next location is examined and so on until (and including) the location whose address is stored in the upper bound word is reached. The word search command does not change the contents of any word in the user's programs.

#### Example

Say that a program is stored between locations 200 and 377 in memory. Then typing in

```
      M0000 7000LF
0054/0000 200LF
0055/0000 377CR
      1000W
```

causes all TAD instructions in the program (those words beginning with the octal digit 1) to be printed out, for example

```
      0210 /1305
      0265 /1305
      0354 /1711
```

if these were the only words in the program containing TAD instructions. Then typing in

```
      M7000 7777CR
      0W
```

would cause all words in the program whose contents were exactly 0000 to be printed out.

## Typing in

M7777 OLF  
0054 /0200 25OLF  
0055 /0377 300CR  
OW

will cause the contents of all words whose addresses are between 250 and 300 (inclusive) to be printed out.

## TAPE PUNCHING COMMANDS

The following commands can be used to punch out paper tapes that can be read in by the BIN loader.

### T - punch leader/trailer

Typing a T will cause about four inches of leader/trailer tape (tape punched with 200 octal) to be punched. The T command also causes the accumulated checksum to be set to zero (cleared). The tape punch should be turned on immediately after the T command is given, and turned off when the Teletype stops punching. Note that as long as the punch is turned on, anything (including commands) typed into the Teletype will be punched onto the tape, so the punch should always be turned off before giving a command.

### P - punch tape

Typing an octal number, nnnn, followed by a semi-colon (;) followed by a second octal number, mmmm, followed by a P, causes a tape corresponding to the contents of the block of memory beginning at location nnnn and ending at location mmmm to be punched. No checksum is punched at the end of the block so that several blocks can be punched together with one inclusive checksum. The punch should be off when the P command is given (otherwise the P command will be punched on the tape, causing errors when the tape is read). ODT halts before punching the tape to give the user time to turn on the punch. ODT is restarted by pressing the RUN/HLT button. Thus, the sequence of actions is: Make sure the punch is off; give P command; ODT halts; turn on punch; press RUN/HLT switch; ODT punches tape; turn off punch.

### E - punch checksum and trailer

Typing an E will cause the accumulated checksum to be punched, followed by about four inches of leader/trailer tape. The checksum is also reset to zero (cleared). Like the P command, ODT halts after the command is typed to give the user time to turn on the punch. The sequence of actions to use is: Make sure punch is off; give E command; ODT

halts; turn on punch; press continue switch; ODT punches checksum and trailer; turn off punch.

#### Example

Say that the user program occupies locations 100 through 170 and 200 through 377 of memory. To punch out the program, the user should give the following commands to ODT:

```
T
T00;170P
200;377P
E
```

#### ERRORS

If an error occurs while typing a command, ODT types "?", ignores the erroneous command and waits for a new one. ODT detects that an error occurred if the user types more than four digits for an octal number or if a decimal digit (8 or 9) is typed. It also detects an error if some character other than a command character (/, carriage return, line feed, ↑, ←, G, B, C, A, M, W, T, ;, P, E) is typed. ODT does not detect errors such as leaving the punch on while typing a command or giving a numerical argument for a command that should have none. For example, 100A is not detected as an error and is treated the same as typing A. If the user makes a mistake while typing a number, he can abort the command by purposely creating an error by typing too many digits or an illegal character.

#### Example

Here are some errors in ODT commands.

<u>12345?</u>	too many digits
<u>248?</u>	decimal digit
<u>101X?</u>	illegal character

#### FIELD COMMANDS

Intersil ODT also contains commands for working with multiple fields. These commands have no effect in the SAMPLER system, which does not contain hardware for handling fields.

The additional command symbols are period (.) and Q. Hence, these are not illegal symbols and typing them does not cause ODT to recognize an error, but it may cause ODT to act strangely. If the user types one of these in by mistake, and ODT acts strangely, he should restart ODT.

Occasionally, ODT may print out 0. (a zero followed by a period) on the terminal. This just means that ODT thinks that you are operating in field zero (which you are) and may be ignored.

#### INTERRUPTS DURING ODT

The ODT program does not use any interrupt features. It does not disable the interrupt system if it is enabled by the user. The user must be cautious when enabling interrupt requests while the user program is 'controlled' by ODT via break points.

## USEFUL SUBPROGRAMS

Some of the subprograms in the ODT ROM are general subroutines that the user may find it useful to call from his own program. These are described below.

### CALLING AND RETURNING FROM A SUBROUTINE

For programs designed to exist in ROM, the JMS instruction cannot be used to call a subroutine because the first word of the routine cannot be used to store the return address. Instead, the ODT program makes use of a stack of return addresses kept in RAM by means of special call and return routines. (Refer to Applications Note M008: ROM Based Subroutine Calls With The IM6100.)

The user may make use of the ODT subroutine stack mechanism as follows. To call a routine requires two consecutive words; the octal number 4056 should be stored in the first word, and the address of the subroutine to be called should be in the second. Executing this piece of code causes the return address to be pushed onto the top of the stack and for control to be transferred to the given address. The AC, link and MQ are unaffected by executing the call instruction.

To return from the subroutine, execute a location containing the octal number 5461. This causes a return address to be taken from the top of the stack and returns control to that location. The AC, link and MQ are again unaffected.

The return address stack begins in location 75 and grows upwards (i.e. the first address pushed goes into location 75, the next into location 76, etc.) ODT may nest subroutines up to three deep, therefore, three stack locations in addition to what the user program needs should be reserved for use by ODT.

The stack pointer is stored in location 62, that is, location 62 contains the address of the location where the most recently pushed return address is stored.

**IMPORTANT NOTE:** The read and change commands in ODT involve calling a subroutine. Hence, giving the command "62/" will print out a value one greater than the true value of the stack pointer.

The user must be cautious when using the software stack for servicing interrupt requests since the software stack routine saves AC in a temporary location which could get overwritten by calling the stack routine again by an interrupt service routine before the first routine could exit.



## TYPE SUBROUTINE

The TYPE subroutine prints on the Teletype the character whose ASCII code is stored in the AC. It also clears the AC before returning.

The subroutine may be called by using the call procedure described in the previous section. The starting address is 6102. Allocate one position on the stack for its use.

## PNUM SUBROUTINE

The PNUM subroutine prints on the Teletype the contents of the AC as a four digit octal number followed by a space. It also clears the AC before returning. The starting address is 6107.

The subroutine may be called by using the call procedure described above. It makes use of the TYPE routine described above, therefore, two stack positions should be allocated for its use.

### Example

Here is a subroutine that counts from 0000 to 7777 octal, printing each number followed by a space and an asterisk on the Teletype.

```
CALL      = 4056
RETURN    = 5461
TYPE      = 6102
PNUM      = 6107
```

200	7200	COUNT, CLA	/set count to zero
201	3214	DCA X	/count stored in X
202	1214	LOOP, TAD X	/get count
203	4056	CALL	
204	6107	PNUM	/print it and space
205	1213	TAD ASTRSK	
206	4056	CALL	
207	6102	TYPE	/print asterisk
210	2214	ISZ X	/increment count
211	5202	JMP LOOP	/repeat loop
212	5461	RETURN	/return
213	0252	ASTRSK, 252	/ASCII for asterisk
214	0000	X, 0	/count stored here, this must be a RAM location (all others can be ROM)

Three stack locations should be reserved for calling COUNT, one for the return address from COUNT itself, and two for using the PNUM subroutine. A stack location need not be allocated for calling TYPE because the two locations required for PNUM are unused when it is called. (In general, for each subroutine reserve one location plus the maximum number of locations required by all the subroutines called by it.)

Since three stack locations should also be reserved for ODT and the stack begins in location 75, locations 75 through 102 should be reserved for use by the stack.

## PROGRAM LISTING:

ODTLIST OUTLIST UOTLIST

```

57. BEGIN PASS 2
58. 1
59. 2 /CDT-F VERSION 5 TAPE 1
60. 3 /CDT-F
61. 4 /CTAL DEBUGGING TECHNIQUE PROGRAM
62. 5 /WITH CAPACITY FOR HANDLING FIELDS
63. 6 /AND BROKEN INTO RCM AND RAM SECTIONS
64. 7
65. 8 / COMMANDS ARE THE SAME AS FOR DEC ODT PLUS
66. 9
67. 10 / N. -- SET CURRENT FIELD TO FIELD N
68. 11 / (CURRENT FIELD IS SAME AS INSTRUCTION FIELD)
69. 12 / A<LF><LF> -- OPENS A REGISTER EQUIVALENT TO M0
70. 13 / A<LF><LF><LF> -- OPENS A REGISTER CONTAINING DATA FIELD
71. 14 / THESE COMMANDS HAVE NO EFFECT ON PRODUCE HARMLESS GARBAGE
72. 15 / ON INS100 SYSTEMS AND OTHER SYSTEMS WHICH DO NOT HAVE
73. 16 / EPA CAPABILITY.
74. 17
75. 18 / L. -- LOAD FROM THE TAPE READER USING BIN FORMAT
76. 19 / (WILL IGNORE CHANGE FIELD CHARACTERS)
77. 20 / TO USE COMMAND, GIVE AN L AFTER THE PROMPT THEN PLACE TAPE
78. 21 / IN READER ON LEADER-TRAILER AND START THE TAPE READER.
79. 22 / THE BIN TAPE WILL BE READ INTO THE CURRENT FIELD AND
80. 23 / THE CHECKSUM WILL BE PRINTED OUT ON THE TTY FOLLOWING
81. 24 / THE END OF THE LOAD.
82. 25
83. 26
84. 27
85. 28
86. 29
87. 30
88. 31
89. 32 / ***** DEFINITIONS *****
90. 33 / F1=C /FIELD CDT-F IS IN
91. 34 / F1A=00 /FIELD CDT-F IS IN, IN BITS 6-8
92. 35 / ZPAT=5 /FIRST ADDRESS FOR THE WORDS OF BREAKPOINT
93. 36 / START=6000 /LINKAGE IN FIELD OF PROGRAM TO BE DEBUGGED
94. 37 / START=6000 /STARTING LOCATION FOR ODT-F
95. 38 / THE I/C INSTRUCTIONS
96. 39
97. 40 6160 RLART=6160
98. 41 6161 RLART=6161
99. 42 6171 RTTY=6171
100. 43 6162 SKPER=6162
101. 44 6163 SKPTR=6163
102. 45 6165 MCR=6165
103. 46 6175 MCR=6175
104. 47 6174 MVR=6174
105. 48 6166 SFLAG1=6166
106. 49 6167 SFLAG2=6167
107. 50 6176 SFLAG3=6176
108. 51 6177 SFLAG4=6177
109. 52 6172 SKIF=6172
110. 53 6173 SKIF=6173
111. 54 /THE FOLLOWING DEFINE LOCATIONS OF VARIABLES
112. 55 / ALL VARIABLES ARE IN PAGE 0
113. 56 0020 TEMP=20 /TEMPORARY STORAGE
114. 57 0021 TEMP2=21 /MORE TEMPORARY STORAGE
115. 58
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176. 118 0072 IFSET=72
177. 119 0073 DFSET=73
178. 120 /FIRST LOCATION OF RETURN ADDRESS STACK
179. 121 0074 STACK1=74
180. 122 PAUSE
181. 123 /CDI-F VERSION 5 TAPE 2
182. 124 /***** FIRST RGP PAGE
183. 125 *START
184. 126 /***** INITIALIZE ROUTINE *****/
185. 127 INIT, CLA
186. 128 0001 TAD SUB1 /INIT SUBROUTINE LINKAGE
187. 129 0002 TAD CALL1+1
188. 130 0003 TAD SUB2
189. 131 0004 TAD CALL1+2
190. 132 0005 TAD SUB3
191. 133 0006 TAD RETR1
192. 134 0007 TAD STACK1
193. 135 0010 TAD CALL1
194. 136 0011 TAD RAM1 /INIT RAM PROGRAM LOCATIONS
195. 137 0012 TAD NEWIAS+1
196. 138 0013 TAD RAM2
197. 139 0014 TAD NEWIAS+2
198. 140 0015 TAD RAM3
199. 141 0016 TAD CFSET+1
200. 142 0017 TAD TRAP3 /INIT BKPT JUMP LINKAGE
201. 143 0018 TAD ZPAT1
202. 144 0019 TAD TRAP4
203. 145 0020 TAD ZPAT1+1
204. 146 0021 TAD CMA /SET CFSAVE TO "UNDEFINED" VALUE
205. 147 0022 TAD CFSAVE
206. 148 0023 TAD CACF /START IN FIELD C
207. 149 0024 TAD WCRB
208. 150 0025 TAD WCRB
209. 151 0026 TAD WCRB
210. 152 0027 TAD WCRB
211. 153 0028 TAD WCRB
212. 154 0029 TAD WCRB
213. 155 0030 TAD WCRB
214. 156 0031 TAD WCRB
215. 157 0032 TAD WCRB
216. 158 0033 TAD WCRB
217. 159 0034 TAD WCRB
218. 160 0035 TAD WCRB
219. 161 0036 TAD WCRB
220. 162 0037 TAD WCRB
221. 163 0038 TAD WCRB
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224. 166 0041 TAD WCRB
225. 167 0042 TAD WCRB
226. 168 0043 TAD WCRB
227. 169 0044 TAD WCRB
228. 170 0045 TAD WCRB
229. 171 0046 TAD WCRB
230. 172 0047 TAD WCRB
231. 173 0048 TAD WCRB
232. 174 0049 TAD WCRB
233. 175 0050 TAD WCRB
234. 176 0051 TAD WCRB
235. 177 0052 TAD WCRB
236. 178 0053 TAD WCRB

/***** CALL AND RETRN ROUTINES *****/
/JMS WILL NOT WORK IN A RCP SINCE THE FIRST WORD OF THE

237. 179 /SUBROUTINE CANNOT BE WRITTEN WITH THE RETURN ADDRESS,SSS.
238. 180 /HENCE, A SUBROUTINE IS CALLED BY
239. 181 / CALL
240. 182 /***** SUBROUTINE NAME
241. 183 /AND RETURNED FROM BY
242. 184 / RETURN
243. 185 /WHERE
244. 186 0054 CALL+JMS CALL1 /CALL SUBROUTINE CPODCE
245. 187 0055 RETRN+JMS RETR1 /RETURN FROM SUBROUTINE CPODCE
246. 188 /CALL THE FOLLOWING ROUTINES THROUGH THE SUBROUTINE
247. 189 /LINKAGE IN PAGE ZERO (SEE TAPE 1)
248. 190 /ROUTINE TO CALL A SUBROUTINE AND PUSH
249. 191 /RETURN ADDRESS ON STACK
250. 192 0061 3043 PLSFJ, DCA ACTEMP /SAVE AC
251. 193 0062 2062 ISZ STACK /ADJUST STACK PTR
252. 194 0063 1056 TAD CALL1 /GET USER RETURN ADDRESS
253. 195 0064 7001 IAC /INCREMENT PAST ARGUMENT
254. 196 0065 3462 DCA I STACK /PUT IT ON STACK
255. 197 0066 1456 TAD I CALL1 /GET USER ENTRY ADDRESS
256. 198 0067 3054 DCA CALL1 /RESTORE AC
257. 199 0068 1043 TAD ACTEMP /RESTORE AC
258. 200 0069 5456 JPP I CALL1 /GET TC USER ROUTINE
259. 201 /ROUTINE TO RETURN FROM A SUBROUTINE, POPPING RETURN ADDRESS
260. 202 /CFF STACK
261. 203 0072 3043 POP, DCA ACTEMP /SAVE AC
262. 204 0073 1462 TAD I STACK /GET RETURN ADDRESS
263. 205 0074 3056 DCA CALL1 /SAVE IT IN CALL1
264. 206 0075 7046 CMA CML /-1 IN AC, COMPLEMENT L
265. 207 0076 1042 TAD STACK /DECREMENT STACK PTR
266. 208 /CAFRY RESTORES L
267. 209 0077 3042 DCA STACK /RESTORE UPDATED STACK PTR.
268. 210 0100 1043 TAD ACTEMP /RESTORE AC
269. 211 0101 5456 JPP I CALL1 /RETURN
270. 212 /***** PRINT SUBROUTINES *****/
271. 213 /SUBROUTINE TO TYPE A CHARACTER
272. 214 /CHAR ASSUMED IN AC
273. 215 0102 6141 TYPE, RUART /CONVERT CHAR TO TTY
274. 216 0103 6142 SMTPT /READY FOR NEXT CHAR?
275. 217 0104 5303 JPP --1 /LCCP IF NOT
276. 218 0105 7200 CLA /CLEAR AC
277. 219 0106 5441 RETURN /RETURN
278. 220 /SUBROUTINE TO PRINT A NUMBER
279. 221 /PRINT CONTENTS OF AC IN OCTAL FOLLOWED BY A SPACE
280. 222 0107 3020 PNUM, DCA TEMP /SAVE NUMBER
281. 223 0110 1324 TAD M4 /INITIALIZE COUNT OF DIGITS
282. 224 0111 3023 TAD TCTE /PRINTED
283. 225 0112 1020 TAD TEMP /GET EACH NUMBER
284. 226 0113 7004 RAL /FIRST SHIFT INTO LINK
285. 227 /LOOP 4 TIMES --, PRINT A DIGIT
286. 228 0114 7004 PNUM2, RAL /SHIFT AC/L THREE LEFT
287. 229 0115 7200 RTI /RETURN
288. 230 0116 3020 DCA TEMP /SAVE NUMBER
289. 231 0117 1020 TAD TEMP /GET IT BACK
290. 232 0120 0325 AND PT /ISOLATE DIGIT
291. 233 0121 1340 TAD P2C /CONVERT TO ASCII
292. 234 0122 4054 CALL, TYPE /TYPE IT OUT
293. 235 0123 6102 TAD TEMP /GET EACH AC (NOTE & STILL SAME)
294. 236 0124 1020 ISZ TCTE /FOURTH ITERATION?
295. 237 0125 2023 JPP PNUM2 /LCCP IF NOT.
296. 238 /PRINT A SPACE
297. 239

298. 239 0127 7200 CLA
299. 240 0130 1337 TAD P240 /GET ASCII CODE FOR SPACE
300. 241 0131 4056 CALL, TYPE /TYPE IT OUT
301. 242 0132 6102 PAUSE
302. 243 0133 5441 /*** RETURN /RETRN
303. 244 /*** CONSTANTS
304. 245 M4, --1
305. 246 0134 7774 P7, 7
306. 247 0135 0007 P7, 7
307. 248 0136 0007 P7, 7
308. 249 0137 0240 P24C, 240
309. 250 0138 0240 P26C, 260
310. 251 0139 0260 P26C, 260
311. 252 /CDI-F VERSION 5 TAPE 3
312. 253 /***** SECOND RGM PAGE
313. 254 *START+200
314. 255 /***** COMMAND SCANNER *****/
315. 256 /THE COMMAND SCANNER INPUTS A COMMAND OF THE FORM
316. 257 /(<NUMBER>)<CHAR> (WHERE THE NUMBER IS OPTIONAL).
317. 258 /IT STORES THE NUMBER IN "NRC" AND JUMPS TO THE
318. 259 /ROUTINE ASSOCIATED WITH THE CHARACTER.
319. 260 /INITIALIZE NUMBER SCANNER
320. 261 REAC, CLA
321. 262 DCA WCRB /SET WCRB TO ZERO
322. 263 TAD SP5
323. 264 DCA TCTE /SET TCTE TO -5
324. 265 /INPUT A CHAR
325. 266 REAC1, SMTPT /CHAR IN INPUT BUFFER?
326. 267 JPP --1 /LCCP IF NOT
327. 268 CLA /PUT CHAR INTO AC
328. 269 AND K0377, DCA SCHAR /SAVE CHAR
329. 270 0141 3025 /ECHO CHAR ON TTY
330. 271 TAD SCHAR /RETRIEVE CHAR
331. 272 CALL TYPE /TYPE IT OUT
332. 273 /NUMBER SCANNER
333. 274 /SEE IF SCHAR IS OCTAL DIGIT
334. 275 TAD SCHAR /GET CHAR ("0"=260,"7"=267)
335. 276 SUB 270 ("0"=10,"7"=11)
336. 277 SNA /IF AC NOT NEG, THEN CHAR HAS
337. 278 /CODE GTR THAN THAT OF DIGIT
338. 279 /SC GO TO CHAR HANDLER.
339. 280 JPP REAC2 /ACD IG ("0"=0,"7"=7)
340. 281 TAD SFIC /IF AC NEG, THEN CHAR HAS
341. 282 SPA /CODE LESS THAN THAT OF DIGIT
342. 283 JPP REAC2 /SL GO TO CHAR HANDLER
343. 284 /ADE DIGIT TO PARTIAL NUMBER IN WORD
344. 285 DCA TEMP /SAVE DIGIT
345. 286 TAD WCRB /GET NUMBER SC FAR
346. 287 CLL RAL /SHIFT AC LEFT THREE BITS
347. 288 RTI
348. 289 TAD TEMP /ACD IN NEW DIGIT
349. 290 DCA WCRB /SAVE RESULT IN WCRB
350. 291 /CHECK FOR TOO MANY DIGITS, RETURN TO CHAR RCP
351. 292 ISZ TCTE /5 DIGITS TYPED?
352. 293 JPP REAC1 /NG - GET NEXT CHAR
353. 294 JPP ERRCR /YES - GO TO ERRCR
354. 295 /CHAR SCANNER
355. 296 /FIND INPUT CHAR IN TABLE AND JUMP TO THE
356. 297 /ASSOCIATED ROUTINE IN TABLE2
357. 298
358. 299 0235 7200 /INITIALIZE SEARCH-LCCP
359. 300 REAC2, CLA
360. 301 TAD BLIST /GET PTR TO BEGIN OF TABLE1
361. 302 DCA SPINTER /INIT FTR INTO TABLE1
362. 303 /SEARCH-LCCP
363. 304 REAC3, TAD I SPINTER /GET CHAR FROM TABLE1
364. 305 ISZ SPINTER /POINT SPINTER AT NEXT CHAR
365. 306 SPA /SKIP IF AFT END OF TABLE1
366. 307 JPP ERRCR /TABLE1 FOLLOWED BY NEG #1
367. 308 CJA /CHAR ACT IN TABLE1 - ERRCR
368. 309 TAD SCHAR /NEGATIVE TABLE CHAR
369. 310 SZA CLA /ACC INPUT CHAR
370. 311 JPP REAC3 /SLM ZERO IF CHARS SAME, SKIP
371. 312 /IF SC
372. 313 /OTHERWISE, CONTINUE LCCP
373. 314 /JUMP TO ASSOCIATED ROUTINE IN TABLE2
374. 315 TAD SPINTER /GET FTR INTO TABLE1
375. 316 /LSPNTR NEW POINTS ONE PAST
376. 317 /MATCHING CHAR.
377. 318 DCA TEMP /CONVERT INTO PTR INTO TABLE2
378. 319 TAD I TEMP /INTC AC
379. 320 DCA TEMP /JUMP TO LOC POINTED TO
380. 321 JPP I TEMP /BY TABLE ENTRY
381. 322 BLIST, TAD TABLE1 /POINTING TO BEGINING OF TABLE1
382. 323 /CONSTANT TO GET CORRESPONDING
383. 324 /LCC IN TABLE2
384. 325 /*** COMMAND SCANNER TABLE ***
385. 326 /EACH ENTRY IN TABLE1 CONTAINS A CHAR.
386. 327 /THE CORRESPONDING ENTRY IN TABLE2 CONTAINS A
387. 328 /PROCEDURE ASSOCIATED WITH THAT CHAR.
388. 329 TABLE1, 215 / CR
389. 330 212 / LF
390. 331 257 / /
391. 332 256 / /
392. 333 301 / A
393. 334 315 / P
394. 335 337 / -
395. 336 336 / B
396. 337 307 / C
397. 338 303 / C
398. 339 327 / W
399. 340 320 / P
400. 341 305 / E
401. 342 324 / T
402. 343 321 / G
403. 344 273 / I
404. 345 314 / L
405. 346 302 7777 /-1 /TABLE FOLLOWED BY NEG NUMBER
406. 347 0303 6616 /CRC
407. 348 0304 6625 /LFOO
408. 349 0305 6601 /SLCC
409. 350 0306 6654 /OCTOC
410. 351 0307 6727 /ACD
411. 352 0310 6730 /BDC
412. 353 0311 6715 /BAGC
413. 354 0312 6670 /UAGC
414. 355 0313 7215 /BCO
415. 356 0314 7200 /CCO
416. 357 0315 7000 /CCO

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420. 358 6316 73C3 NDC
421. 359 6317 75C2 PCC
422. 360 6320 74E4 EDC
423. 361 6321 7471 TDC
424. 362 6322 7454 UCC
425. 363 6323 7451 SEMICO
426. 364 6324 757E BIN-2
427. 365
428. 366 6322 *START+332
429. 367
430. 368
431. 369
432. 370 6332 7510 / *** CONSTANTS
433. 371 6333 7773 SP270, -270
434. 372 6334 001C SP5, -5
435. 373 6335 0277 SP1C, 10
436. 374 6336 C377 K0377, Q377:FALSE
437. 375
438. 376 6337 *START+337
439. 377
440. 378
441. 379
442. 380 6337 72CC / ***** ERROR HANDLER *****
443. 381 6340 1335 /TYPE *M* AND RETURN TO COMMAND SCANNER
444. 382 6341 405E ERROR, CLA
445. 383 6342 61CC TAD SP277 /GET QUESTION MARK
446. 384 6343 405E CALL /TYPE IT CUT
447. 385 6344 64CC TYPE /TYPE CR,LF
448. 386 6345 52CC CALL JMP READ
449. 387
450. 388
451. 389
452. 390
453. 391
454. 392 /COT-F VERSION 5 TAPE 4
455. 393 / ***** EXAMINE DEPOSIT ROUTINES *****
456. 394 6400 /SUBROUTINE TO TYPE CR, LF AND SHUT REGISTER
457. 395 /START+400
458. 396 6400 1257 /CR,LF, TAD TP215 /GET CR
459. 397 6401 405E CALL
460. 398 6402 61CC TYPE /TYPE IT
461. 399 6403 125E TAD TP212 /GET LF
462. 400 6404 405E CALL
463. 401 6405 61CC TYPE /TYPE IT
464. 402 6406 704C CPA /SET AC TO -1
465. 403 6407 3032 DCA SHUT /STORE IN SHUT
466. 404 6408 54E1 RETURN /RETURN
467. 405
468. 406 /SUBROUTINE TO CLOSE REG
469. 407 6411 2032 /SEE IF REG ALREADY SHUT
470. 408 6412 741C CLOSE, ISZ SHLT /SKIP IF SHUT=-1
471. 409 6413 54E1 SHR /LAST COMMAND
472. 410
473. 411 6414 405E /SEE IF VALUE TYPE IN
474. 412 6415 6423 CALL
475. 413 6416 54E1 NTYPE /SEE IF NUMBER TYPE
476. 414
477. 415 6417 1024 RETURN /RETURN IF REG ALREADY SHUT
478. 416 6420 405E /STORE TYPE VALUE IN REG
479. 417 6421 6442 TAD NCRC /GET NEW VALUE
480. 418 6422 54E1 CALL
481. 419
482. 420 /SUBROUTINE TO SEE IF NUMBER HAS BEEN TYPE
483. 421 /SKIP CR RETURN IF ANY NUMBER HAS BEEN TYPE SINCE
484. 422 6423 1023 NTYPE, TAD TCCE /TCCE IS -5 INITIALLY, AND
485. 423 6424 7041 CIA /INCREMENTED ONLY IF A NUMBER
486. 424 6425 1255 TAD TMS /IS TYPE
487. 425 6426 764C SZA CLA /SKIP IF TCCE=-5
488. 426 6427 24E2 ISZ I STACK /INCREMENT RETURN ADDR
489. 427 6430 54E1 RETURN
490. 428
491. 429 /SUBROUTINE TO GET CONTENTS OF CURRENT LOCATION
492. 430 6431 1030 /NEW CONTENTS RETURN IN AC
493. 431 6432 12CC GETCAD, TAD CACF /GET FIELD OF CURRENT LOC
494. 432 6433 3073 TAD TP62C1 /ACD IN "CCF" INSTRUCTION
495. 433 6434 1261 DCA DFSET /STORE IT IN RAM PROGRAM
496. 434 6435 3042 TAD PFSET /SET RAM PROG TO RETURN TO GETCCL
497. 435 6436 5073 DCA JADR /EXECUTE RAM PROGRAM - CHANGE FIELD
498. 436 6437 5073 JMP DFSET /TC THAT OF CURRENT LOCATION
499. 437 6437 1427 GETCCL, TAD I CAG /GET CONTENTS OF CURRENT LOC
500. 438 6440 62C1 CCF FIA /RESTORE DATA FIELD
501. 439 6441 54E1 RETURN /RETURN
502. 440
503. 441 /SUBROUTINE TO SET CONTENTS OF CURRENT LOCATION
504. 442 6442 302C /NEW CONTENTS PASSED IN AC
505. 443 6443 1030 SETCAD, TAD TEMP /SAVE AC
506. 444 6444 126C TAD CACF /GET FIELD OF CURRENT LOC
507. 445 6445 3073 TAD TP62C1 /ACD IN "CCF" INSTRUCTION
508. 446 6446 1262 DCA DFSET /STORE IT IN RAM
509. 447 6447 3042 TAD PFSET /SET RAM PROG TO RETURN TO SETCCL
510. 448 6448 5073 JMP DFSET /EXECUTE RAM PROGRAM - CHANGE FIELD
511. 449
512. 450 6451 102C SETCCL, TAD TEMP /TC THAT OF CURRENT LOCATION
513. 451 6452 2427 DCA I CAG /RESTORE AC
514. 452 6453 62C1 CCF FIA /SET CURRENT LOC TO NEW VALUE
515. 453 6454 54E1 RETURN /RETURN
516. 454
517. 455 / *** CONSTANTS
518. 456 6456 7773 TP5, -5
519. 457 6457 7215 TP212, 212
520. 458 6458 62C1 TP215, 215
521. 459 6459 62C1 TP62C1, 62C1
522. 460 6460 6437 PGTCCL, GETCCL
523. 461 6461 6451 PSTCCL, SETCCL
524. 462 6462 6172 LISA, SKIP3 / THE ITY LISA ROUTINE FOR THE BIN
525. 463 6463 70CC SFLACL / LCACR
526. 464 6464 616E SKIP2 / SET READER RUN
527. 465 6465 526E JMP -1
528. 466 6466 6167 CFLAG1
529. 467 6467 6162 SAKPR
530. 468 6468 5271 JMP -1
531. 469 6469 72C0 CLA
532. 470 6470 616E MUART
533. 471 6471 6277 AND ITYP
534. 472 6472 54E1 RETURN
535. 473 6473 6377 ITYP, C377
536. 474
537. 475 /COT-F VERSION 5 TAPE 5
538. 476 / ***** EXAMINE/LEPCST ROUTINES - CONTINUED *****
539. 477 / ***** FOURTH RCP PAGE
540. 478 6600 *START+600
541. 479 /TRANSFER ADDRESS TO COMMAND SCANNER
542. 480
543. 481 6600 6200 CCM*, READ
544. 482 /SLASH HANDLER
545. 483 /OPEN LOCATION "CACF", "CAC"
546. 484 6601 1031 /IF NUMBER TYPE, SET CAD
547. 485 6602 3077 SLCC, TAD CACF
548. 486 6603 405E DCA CACF
549. 487 6604 6423 CALL
550. 488 6605 521C NTYPE /SEE IF NUMBER TYPE
551. 489 6606 1024 JMP SLCC1 /AC NLP TYPE, LEAVE CAD ALONE
552. 490 6607 3027 TAD NCRC /ELSE GET NLP TYPE
553. 491 /AND SET CAD TO IT
554. 492 6610 3032 /TYPE CONTENTS OF "CACF", "CAC"
555. 493 6611 405E SLCC1, DCA SHUT /SET REGISTER STATUS TO "OPEN"
556. 494 6612 6431 CALL
557. 495 6613 405E GETCAD /GET CONTENTS OF CURRENT LOC
558. 496 6614 61C7 PNUM /TYPE IT CUT
559. 497 6615 56CC /RETURN TO COMMAND SCANNER
560. 498
561. 499 /CR HANDLER
562. 500 /CLOSE LOCATION
563. 501 6617 6411 CRCC, CALL /CLOSE LOCATION
564. 502 6620 1031 TAD CACF /CLOSE LOCATION
565. 503 6621 3030 DCA CACF /TYPE CR, LF
566. 504 6622 405E CALL /GET NEXT COMMAND
567. 505 6623 640C CRLF
568. 506 6624 56CC JMP I CCM*
569. 507
570. 508 /LF HANDLER
571. 509 6625 405E /CLOSE LOCATION AND OPEN NEXT LOCATION
572. 510 6626 6411 LFDC, CALL
573. 511 6627 1344 TAD SP215 /CLOSE LOCATION
574. 512 6630 405E CALL /GET CR
575. 513 6631 61C2 TYPE /TYPE IT
576. 514 6633 61C2 CALL
577. 515 6634 61C2 TYPE /TYPE A NULL (GIVES TIME FOR CR)
578. 516 6635 2027 ISZ CAC /PCINT CAC AT NEXT LCC
579. 517 6635 70C0 NCP /IN CASE ISZ SKIPS
580. 518 6636 1030 TAD CACF /COMPARE CURRENT AND SAVED FIELDS
581. 519 6637 7041 CIA
582. 520 6640 1031 TAD CACF /DIFFERENT?
583. 521 6641 765C SNA CLA /AC + GC CN
584. 522 6642 5245 JMP LFDC1 /YES - PRINT FIELD
585. 523 6643 405E CALL
586. 524 6644 7347 PFIELD
587. 525
588. 526 /THE FOLLOWING IS AN ENTRY PCINT FOR ANY ROUTINE
589. 527 6645 1027 /THAT TYPES OUT WHAT LOCATION IT IS OPENING.
590. 528 6646 405E LFDC1, TAD CAC /GET CAD
591. 529 6647 61C7 CALL
592. 530 6650 1345 PNUM /TYPE IT CUT
593. 531 6651 405E TAD SP257 /GET ASCII FOR "M"
594. 532 6652 61C2 CALL
595. 533 6653 521C TYPE /TYPE IT CUT
596. 534 JMP SLCC1 /REST IS LIKE OPEN REG ROUTINE
597. 535
598. 536 /P. HANDLER
599. 537 6654 1024 /SET FIELD OF CURRENT LOCATION
600. 538 6655 71C4 CTECC, TAD NCRC /GET FIELD SPEC FROM NCRC
601. 539 6656 70C0 CLL RAL /FIELD SPEC INTO BITS 6-8
602. 540 6657 303C RTL /FIELD SPEC INTO BITS 6-8
603. 541 6658 103C UCA CACF /STORE IT AS CURRENT FIELD
604. 542 6659 TAD CACF
605. 543
606. 544 6661 3021 DCA CACF /CFSAVE INITIALIZED?
607. 545 6662 1052 TAD CFSAVE
608. 546 6663 71CC SNA CLA /CFSAVE INITIALIZED?
609. 547 6664 56CC JMP I CCM* /YES - RETURN
610. 548 6665 103C JMP I CCM* /NO - INIT CFSAVE TO CACF
611. 549 6666 3052 TAD CFSAVE /GET NEXT COMMAND
612. 550 6667 56CC JMP I CCM*
613. 551
614. 552 6670 405E /HANDLE
615. 553 6671 6411 /PRETEND CAC IS A MEMORY REFERENCE INS AND OPEN
616. 554 6672 765E LADC, CALL /LOCATION REFERENCED. INCREASE I BIT.
617. 555 6673 640C CALL /CLOSE LOCATION
618. 556 6674 405E CRLF /TYPE CR,LF
619. 557 6675 6431 CALL
620. 558 6676 3020 GETCAD /GET CONTENTS
621. 559 6677 1631 DCA TEMP /SAVE THEM IN TEMP
622. 560 6678 3030 TAD CACF
623. 561 6679 102C TAD TEMP /GET BACK CONTENTS
624. 562 6680 3021 AND SP177 /ISLATE PAGE ADDR BITS
625. 563 6681 102C DCA TEMP2 /AND SAVE THEM IN TEMP2
626. 564 6682 6343 TAD TEMP /GET CONTENTS AGAIN
627. 565 6683 6706 AND SP2CC /ISOLATE PAGE ZERO BIT
628. 566 6684 5312 SNA CLA /REFERENCE TO PAGE ZERO?
629. 567 6685 1027 JMP LADC1 /YES - SKIP NEXT CODE
630. 568 6686 0351 TAD CAC /NO - GET CURRENT ACD
631. 569 6687 1021 AND SP7CC /ISLATE PAGE NUMBER
632. 570 6688 3027 DCA CAC /ACD IN PAGE ADDR
633. 571 6689 5245 JPP LFCC1 /PLT INTO CAC
634. 572
635. 573 /P. HANDLER
636. 574 6691 405E /OPEN LOC PCINTED AT BY CURRENT LOC
637. 575 6692 6411 BADC, CALL /CLOSE CURRENT LOC
638. 576 6693 405E CRLF /TYPE CR,LF
639. 577 6694 640C CALL
640. 578 6695 405E CALL /GET CONTENTS OF CURRENT LOC
641. 579 6696 6431 GETCAD /MAKE IT INTO NEW LOC
642. 580 6697 3627 DCA CAD
643. 581 6698 1031 TAD CACF
644. 582 6699 363C DCA CACF
645. 583 6700 5245 JPP LFCC1 /REST LIKE LF
646. 584
647. 585 /A.M.I HANDLER
648. 586 6701 1337 ACC, TAD ACCR1 /OPEN LOC CONTAINING AC, MASK, OR INSTRUCTION FIELD
649. 587 6702 1340 MDU, TAD ACCR2 /GET REGISTER ADDRESS - ACSAVE
650. 588 6703 3027 DCA CAC /CR MASK
651. 589 6704 103C TAD CACF /PLT INTO CAC
652. 590 6705 3031 DCA CACF
653. 591 6706 1341 TAD ACCR4
654. 592 6707 3030 DCA CACF
655. 593 6708 521C JPP SLCC1 /REST LIKE SLASH
656. 594 6709 7774 ADDR1, ACSAVE+MASK
657. 595 6710 0057 ADDR2, MASK
658. 596 6711 00C0 ADDR3, FIA
659. 597
660. 598 / *** CONSTANTS
661. 599 6712 0177 SF177, 177
662. 600 6713 02CC SP2C0, 2CC
663. 601 6714 0215 SP215, 215
664. 602 6715 0257 SP257, 257

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664.	602	6740	0260	SP260, 26C		786.	724	7144	0200	FP260, 260	
665.	603	6747	0400	SP400, 40C		787.	725	7145	0400	FP400, 40C	
666.	604	6750	6201	SP6201, 6201		788.	726	7146	2000	FP200200C, 200C	
667.	605	6751	76CC	SP7600, 76CC		789.	727	7147	4000	FP4000, 4000	
668.	606			PAUSE		790.	728	7150	7000	FP7000, 7000	
669.	607			/CDI-F VERSION 5 TAPE 6		791.	729	7151	76CC	FP7600, 7600	
670.	608			/ ***** BREAKPOINT/CONTINUE ROUTINES *****		792.	730	7152	777C	FP7770, 777C	
671.	609			/ ***** FIFTH RCP PAGE		793.	731			PAUSE	
672.	610	7000		*START+1000		794.	732			/CDI-F VERSION 5 TAPE 7	
673.	611			/ C HANDLER		795.	733			/ ***** BREAKPOINT/CONTINUE ROUTINES - CONTINUED *****	
674.	612			/CONTINUE EXECUTICA CF PROGRAM		796.	734			/ ***** SIXTH RCP PAGE	
675.	613	7000	1031	CCO, TAD CCADF		797.	735		72CC	*START+1200	
676.	614	7001	3030	CCA CACF		798.	736			/ G HANDLER	
677.	615	7002	4056	CALL		799.	737			/GC TO A SPECIFIED LOCATION	
678.	616	7003	6400	CRLF		800.	738	7400	1031	TAD CCADF	
679.	617	7004	1024	TAD WCRD	/SET CONTINUE COUNT TC	801.	739	7401	3030	CCA CACF	
680.	618	7005	704C	CMA	/ITERATE PAST BREAKPOINT	802.	740	7402	4056	CALL	
681.	619	7006	3044	CCA NCENT	/SPECIFIED NUMBER OF TIMES	803.	741	7403	6400	CRLF	
682.	620			/SIMULATE EXECUTICA CF THE INSTRUCTION IN		804.	742	7404	704C	CMA	/SET CONTINUE COUNT TC ZERC
683.	621			/LOCATICA CCATF.CCAT		805.	743	7405	3044	CCA NCENT	
684.	622			/HANDLE ICT CR OPERATE INSTRUCTION		806.	744	7406	1024	TAD WCRD	/SET ADDR TC CONTINUE PROGRAM
685.	623	7007	1035	CCNT0, TAC CAC	/SET CACF.CAC TO CCATF.CCAT	807.	745	7407	3025	CALL	
686.	624	7016	1027	TAC CAC		808.	746	7410	1030	TAD CACF	/SET FIELD TC CONTINUE PROGRAM
687.	625	7011	1036	TAD IFSAVE	/IFSAVE IS CONTINUE FIELD	809.	747	7411	3026	TAD IFSAVE	/EXECUTION FROM
688.	626	7012	303C	CCA CACF		810.	748	7412	1365	TAD FIFST	/SET RAM PROGRAM EXECUTION TO
689.	627	7013	4056	CALL	/GET CONTINUE INSTRUCTION	811.	749	7415	3043	CCA CACR	/BEGIN AT HFSET
690.	628	7014	6431	GETCAD		812.	750	7414	5767	JMP I FCONT5	/GC TC EXECUTE RAM PROGRAM
691.	629	7015	3037	CCA INS	/STORE IT IN INS	813.	751			/ B HANDLER	
692.	630	7016	2035	ISZ CCAT	/PCINT CCAT AT NEXT INS TC EXECUTE	814.	752			/SET BREAKPOINT AT A SPECIFIED LOCATION	
693.	631	7017	1035	TAD CCAT	/SET ADDR TO CCAT PROG EXEC FROM	815.	753	7415	1031	TAD CCADF	
694.	632	7020	3025	DCA JACR1		816.	754	7416	303C	CCA CACF	
695.	633	7021	1037	TAC INS		817.	755	7417	4056	CALL	
696.	634	7022	710C	CLL		818.	756	7420	6400	CRLF	/TYPE CR,LF
697.	635	7023	1346	TAD FP2000	/CVERFLOW SETS L FOR ICT CR OPER	819.	757	7421	4056	CALL	/SEE IF VALUE SPECIFIED
698.	636	7024	7620	SNA CLA		820.	758	7422	6423	NTYPEC	
699.	637	7025	5233	JMP CCAT1	/JMP IF NCT ICT CR OPER	821.	759	7423	5231	JMP BOCI	/VALTE SPECIFIED - GC CN
700.	638	7026	1027	TAD INS /NEWINS	INS	822.	760	7424	1030	TAD WCRD	/SET BREAKPOINT ADDRESS
701.	639	7027	3087	DCA NEWINS		823.	761	7425	3033	CCA TRAC	
702.	640	7030	703C	TAD NEWINS	/SET RAM PROGRAM EXECUTION TO	824.	762	7426	1030	TAD CACF	/AND FIELD
703.	641	7031	3043	DCA CACR	/GC TC EXECUTE RAM	825.	763	7427	3034	DCA TRADF	
704.	642	7032	5740	JMP I FCONT5	/BEGIN PRCP *NEWINS	826.	764	7428	5770	JMP I FREAD	/GET NEXT COMMAND
705.	643			/PUT EFFECTIVE ADDRESS OF MEMORY REFERENCE INSTRUCTION		827.	765	7429	1371	TAD CCLG	/NG VALUE SPECIFIED -
706.	644			/INTO ADDR		828.	766	7432	3033	DCA TRAF	/ CLEAR BREAKPOINT BY SETTING
707.	645	7033	1037	TAD INS		829.	767	7433	10CC	TAD FIA	/ IT WITHIN CCI-F
708.	646	7034	0343	AND FP177	/GET ADDRESS CN PAGE	830.	768	7434	3034	CCA TRADF	
709.	647	7035	304C	DCA ADDR		831.	769	7435	577C	JMP I FREAD	/GET NEW COMMAND
710.	648	7036	1037	TAD INS	/TEST FOR ZERC PAGE	832.	770			/BREAKPOINT RETURN	
711.	649	7037	0344	AND FP20C		833.	771			/ENTRY TO GDI LPCN ENCOUNTERING BREAKPOINT	
712.	650	7040	703C	SNA CLA		834.	772			/SAVE STUFF THEN GC TC COMPARE SCANNER	
713.	651	7041	5247	JMP CCNT2	/ZERC PAGE - GC CN	835.	773	7236	3047	DCA ACSAVE	/SAVE AC
714.	652	7042	704C	CMA	/CURRENT PAGE - GET INS LOCATION	836.	774	7237	70C4	RAL	/SAVE LINK
715.	653	7043	1035	TAD CCNT	/CCAT PCINTS CNE PAST INS LOC	837.	775	7240	1030	DCA LSARE	/SAVE PG
716.	654	7044	0351	AND FP76CC	/ISOLATE PAGE NUMBER	838.	776	7241	77C1	CCA PCSAVE	/SAVE PROGRAM DATA FIELD
717.	655	7045	604C	TAD ADDR	/JAC TC GET	839.	777	7242	3051	RF	/SAVE FIA
718.	656	7046	3040	DCA ADDR	/NEW EFFECTIVE ADDRESS	840.	778	7243	6214	DCA EFSAVE	/SET CCI-F F-F DATA FIELD
719.	657			/HANDLE INDIRECT REFERENCE		841.	779	7244	3052	CCF FIA	/SAVE PROGRAM INSTRUCTION FIELD
720.	658	7047	1036	CCNT2, TAD IFSAVE	/ASSUME EA IS IN INSTRUCTION FIELD	842.	780	7245	62C1	TAD TRADF	
721.	659	7050	3041	DCA CFI	/SO SET CFI IFSAVE	843.	781	7246	1034	DCA IFSAVE	/AND BREAKPOINT LOCATION
722.	660	7051	1037	TAD INS	/TEST INS FOR INDIRECT	844.	782	7247	3026	TAD TRAD	/FOR CONTINUE
723.	661	7052	0345	AND FP400		845.	783	7250	1033	DCA CCAT	
724.	662	7053	765C	SNA CLA		846.	784	7251	3035		
725.	663	7054	5276	JMP CCNT3	/INDIRECT - GO CN	847.	785	7252	1033	TAD TRAC	/RESTORE BREAKPOINT LOCATION
726.	664	7055	1052	TAD EFSAVE	/INDIRECT - GC EA IS IN DATA FIELD	848.	786	7253	3027	DCA CAC	/CCATENTS
727.	665	7056	3041	DCA CFI	/SO SET CFI IFSAVE	849.	787	7254	1024	TAD TRADF	
728.	666	7057	1040	TAC ADDR	/SET CAD ADDR	850.	788	7255	303C	CCA CACF	
729.	667	7060	3027	CCA CAC		851.	789	7256	1045	TAD KEEP	
730.	668			/HANDLE AUTO-INCREPENT		852.	790	7257	4056	CALL	
731.	669	7061	1040	TAC ADDR	/SEE IF ADDR IS AUTO-INC REGISTER	853.	791	7260	6442	SETCAD	
732.	670	7062	0352	AND FP777C		854.	792	7261	1030	TAD CACF	/INITIALIZE CCADF
733.	671	7063	1342	TAD FP77C		855.	793	7262	3021	DCA CCADF	
734.	672	7064	764C	SZA CLA		856.	794	7263	2044	ISZ NCENT	/CONTINUE COUNT OVER?
735.	673	7065	5273	JMP CCAT2A	/ACT ALTO-INCR - GC CN	857.	795	7264	5766	JMP I FCONT0	/GC - CONTINUE
736.	674	7066	4056	CALL	/ALTE-INCR - GET REGISTER VALUE	858.	796	7265	4056	PFIELE	/TYPE CUI RPT FIELD
737.	675	7067	6431	GETCAD		859.	797	7266	1347	TAD TRAD	/AND LOCATION
738.	676	7070	70C1	IAC	/INCREMENT IT	860.	798	7267	10323	CALL	
739.	677	7071	4056	CALL	/AND RESTORE	861.	799	7270	4056	CALL	
740.	678	7072	6442	SETCAD		862.	800	7271	61C7	PNUM	
741.	679			/FINISH HANDLING INDIRECT		863.	801	7272	1361	TAD AP250	/TYPE *I*
742.	680	7073	4056	CCNT2A, CALL	/GET CCATENTS OF ADDR	864.	802	7273	4056	CALL	
743.	681	7074	6431	GETCAD		865.	803	7274	61C2	TYPE	
744.	682	7075	304C	DCA ADDR	/AND MAKE THAT NEW ADDR	866.	804	7275	1047	TAD ACSAVE	/TYPE CUI AC
745.	683			/HANDLE JMS, JMP INSTRUCTIONS		867.	805	7276	4056	CALL	
746.	684	7076	1037	TAD INS	/SEE IF INS IS JMP CR JMS	868.	806	7277	61C7	PNUM	
747.	685	7077	710C	CLL		869.	807	7300	4056	CALL	/TYPE CR, LF
748.	686	7100	0350	AND FP70CC		870.	808	7301	6400	CRLF	
749.	687	7101	1347	TAD FP40CC		871.	809	7302	577C	JMP I FREAD	/GET NEXT COMMAND
750.	688	7102	742C	SNA CLA		872.	810			/ ***** WCRD SEARCH ROUTINE END *****	
751.	689	7103	5321	JMP CCAT4	/L-1 IF INS IS JMS CR JMP	873.	811			/ W HANDLER	
752.	690	7104	764C	SZA CLA	/NCT JMS CR JMP - GC CN	874.	812			/SEARCH BETWEEN LIMC AND LIM1 FOR WCRDS THAT	
753.	691	7105	5314	JMP CCAT3A	/JMS CR JMP - AC-0 IF INS IS JMS	875.	813			/PATCH SPECIFIED ALPHER IN MASKED BITS	
754.	692	7106	1044	TAD ADDR	/ACT JMS - GO ON	876.	814	7303	1031	WCRD, DCA CACF	
755.	693	7107	3027	DCA CAC	/JMS - EMULATE JMS	877.	815	7304	3030	CALL	
756.	694	7110	1035	TAD CCAT		878.	816	7305	4056	CALL	/TYPE CR,LF
757.	695	7111	4056	CALL		879.	817	7306	6400	CRLF	
758.	696	7112	6442	SETCAD	/SET RETURNRURN ADDRESS	880.	818	7307	1024	TAD WCRD	/MASK SPECIFIED NUMBER
759.	697	7113	204C	ISZ ADDR	/PCINT ADDR AT SUBROUTINE BODY	881.	819	7310	0053	DCA WCRD	
760.	698	7114	1040	TAC ADDR	/GET JUMP DESTINATION	882.	820	7311	3024	CCA WCRD	
761.	699	7115	3025	DCA JACR1	/AND SET IT	883.	821	7312	1054	TAD LPLG	/INITIALIZE CAD AS MEMORY
762.	700	7116	1335	TAD FIFSET	/START RAM PRG EXECUTION	884.	822	7313	3027	DCA CAC	/ POINTER FOR SEARCH
763.	701	7117	3042	DCA G0ADR	/PRCP IFSET	885.	823	7314	4056	CALL	/GET MEMORY WCRD
764.	702	7120	5740	JMP I FCONT5	/GC TC EXECUTE RAM PRG	886.	824	7315	6431	GETCAD	
765.	703			/HANDLE AND, TAD, CCA, ISZ INSTRUCTIONS		887.	825	7316	0053	AND MASK	/MASK IT
766.	704	7122	1027	TAD INS	/GET INS	888.	826	7317	7041	CIA	/CCMPARE TC WCRD
767.	705	7123	0350	AND FP70CC	/ISOLATE GPCOCE	889.	827	7320	1024	TAD WCRD	
768.	706	7124	1337	TAD IACDR		890.	828	7321	764C	SZA CLA	/EQUAL?
769.	707	7125	3087	DCA NEWINS	/SET NEWINS OPCOCE I ADDR	891.	829	7322	5327	JMP WCC2	/NE CNE - GET NEXT
770.	708	7126	1041	TAD CFI		892.	830	7323	1027	TAD CAC	/YES - PRINT LOC
771.	709	7127	1341	TAD CFCF		893.	831	7324	4056	CALL	
772.	710	7130	3066	DCA CFSET1	/SET CFSET1 CDF CFI	894.	832	7325	61C7	PNUM	
773.	711	7131	1236	TAD CFSET1	/SET RAM PRG EXECUTION	895.	833	7326	1363	TAD AP257	/PRINT *I*
774.	712	7132	3043	DCA CACR	/ TO REGIA PRCP DFSET1	896.	834	7327	4056	CALL	
775.	713	7133	5740	JMP I FCONT5	/GC TC EXECUTE RAM PRG	897.	835	7330	61C2	TYPE	
776.	714			/ *** CONSTANTS		898.	836	7331	4056	CALL	/PRINT CCATENTS
777.	715	7134	0067	PAWINS, NEWINS		899.	837	7332	6431	GETCAD	
778.	716	7135	0072	PIFSET, IFSET		900.	838	7333	4056	CALL	
779.	717	7136	0066	PIFSET1, DFSET1		901.	839	7334	61C7	PNUM	
780.	718	7137	0440	IACDR, 400 ADDR		902.	840	7335	4056	CALL	/TYPE CR, LF
781.	719	7140	7400	PCNTS, CCNTS		903.	841	7336	6400	CRLF	
782.	720	7141	62C1	CFPC, CDF		904.	842	7337	1027	TAD CAD	/GET LCC
783.	721	7142	7770	FMIC, -10		905.	843	7340	2027	ISZ CAD	/PCINT AT NEXT



## APPENDIX C

### INTERSIL 6960-SAMPLR BOARD

- NOTE: 1. Square pads are used for pin 1 of all integrated circuits, the emitter of all transistors, the cathode of all diodes, and the common terminal of all switches.
2. Push button switches are assumed to have common as one end terminal, the center terminal normally open and the other end terminal normally closed (C & K type 8121). Toggle switches are assumed to have common in the center (C & K type 7101).

#### JUMPER OPTIONS

J1 CO (6100) to VCC  
J2 C2 (6100) to VCC  
J3 INTREQ (6100) to VCC  
J4 CPREQ (6100) to VCC  
J5 DMAREQ (6100) to VCC  
J6 WAIT (6100) to VCC  
J7 (Deleted)  
J8 SENSE4 (6101) to GND  
J9 PRIN (6101) to GND  
J10 INTGNT (6101) to GND  
J11 SEL4 (6101) to GND  
J12 SEL4 (6101) to VCC  
J13 SEL7 (6101) to VCC  
J14 SEL7 (6101) to GND  
J15 Deleted  
J16 Deleted  
J17 OSC IN (6100) to pin 3 IC35 (74C00)  
J18 OSC OUT (6100) to pin 11 IC35 (74C00)  
J19 TRC (6402) to pin 2 (7213)  
J20 RRC (6402) to pin 2 (7213)  
J21 RESET (6100) to pin 1 IC26 (74C74)  
J22 XTB (6100) to S16 common  
J23 WAIT (6100) to pin 6 IC26 (74C74)  
J24 IFETCH (6100) to pin 1 IC16 (74C00)  
J25 TRO (6402/03) to RS-232-C XMT level shifter  
J26 TRO (6402/03) to 20 mA loop XMT level shifter  
J27 GND to 20 mA loop XMT level shifter  
J28 RRI (6402/03) to RS-232-C RCVE level shifter  
J29 RRI (6402/03) to 20 mA loop RCVE level shifter  
J30 FLAG1 (6101) to 20 mA RDR RUN loop driver  
J31 GND to -12V point of 20 mA loop interface  
J32 -12V terminal to -12V point of 20 mA loop interface  
J33 Pin 1 of EIA connector to GND

## JUMPER STRAPPING

### OPTION 1 - BASIC MICROCOMPUTER (See Figure 1)

Insert the following jumpers: J1, J2, J3, J4, J5, J6, J8, J9,  
J10, J11, J13

Note: J11 and J12 should NEVER be in circuit at the same time.

Similarly, J13 and J14 should NEVER be in circuit at the same time.

If both jumpers were in, a direct short circuit between VCC and GND would result.

XTAL 11 and XTAL 12 should be used.

### OPTION 2 - 20 mA CURRENT LOOP INTERFACE (See Figure 2)

Insert jumpers J26 and J29

### OPTION 3 - PROGRAM CONTROLLED TELETYPE PAPER TAPE READER

Insert jumper J30.

### OPTION 4 - EIA RS-232-C INTERFACE (See Figure 3)

Insert jumpers J25, J28, J27.

Note: Options 2 and 4 are mutually exclusive, so insert and delete jumpers as necessary.

### OPTION 5 - IM6402 UART TIMING (See Figure 4)

Use XTAL14 (delete XTAL21), insert jumpers J19 and J20

### OPTION 6- SINGLE CLOCK OPERATION (See Figure 5)

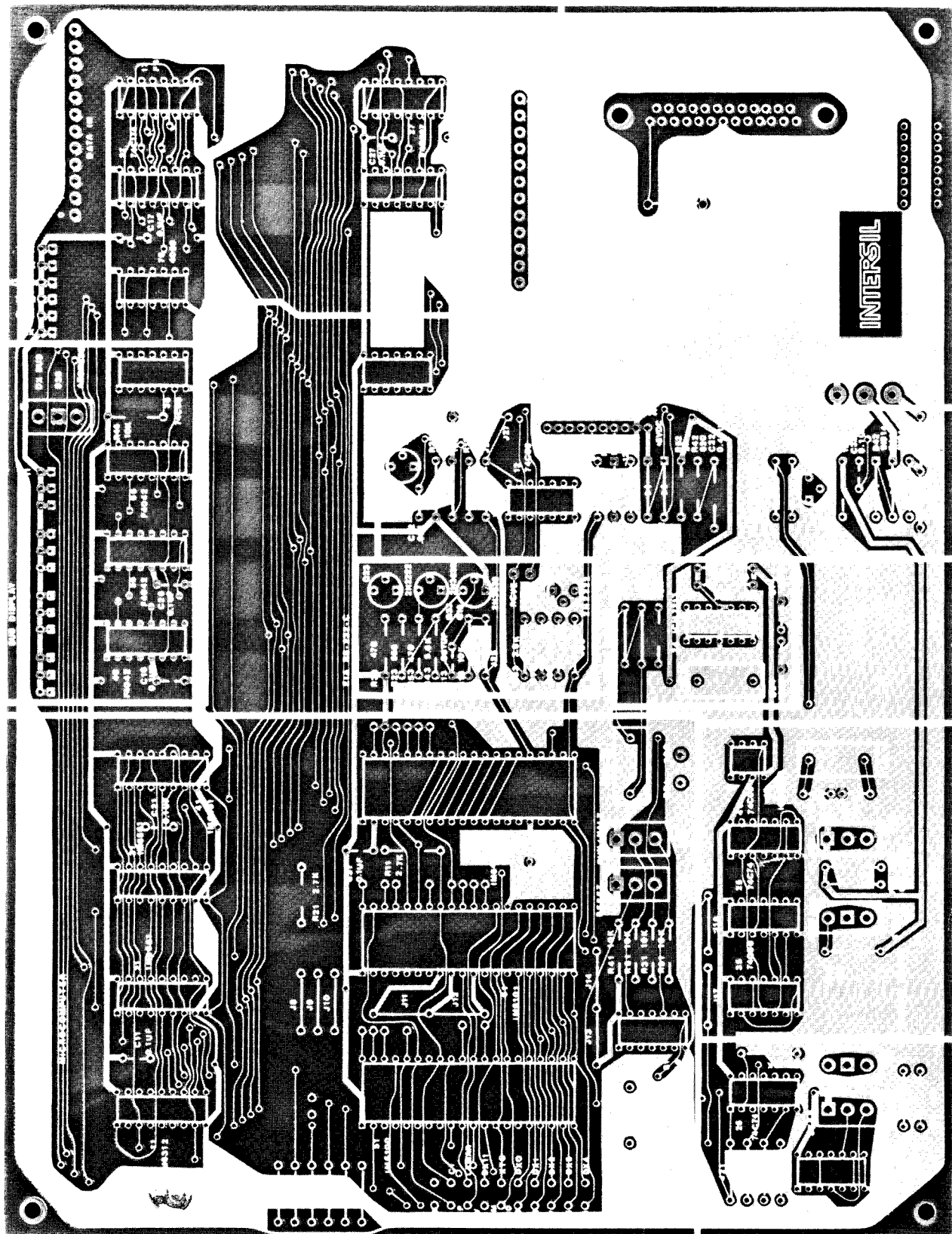
Use XTAL15 (delete XTAL11), insert jumpers J17 and J18

### OPTION 7 - SINGLE STEPPING OPERATION (See Figure 6)

Delete jumper J6 and add jumpers J21, J22, J23 and J24



APPENDIX C  
6960 SAMPLR BOARD



## APPENDIX D

### TELETYPE MODIFICATIONS FOR THE INTERCEPT SYSTEM

The Intersil INTERCEPT systems have been designed to be used in conjunction with a Model ASR-33 Teletype. Before attempting to use your system inspect your Teletype for the following modifications and additions. If they have not yet been performed, you must complete them before using INTERCEPT.

To check for, or make, these modifications remove the cover of the Teletype. Loosen the three thumb screws in the back and remove the Platen that holds the roll of paper, the Mode Switch knob and the Face Plate. Remove the small screw on the Reader cover and the four screws under the Face Plate. You should now be able to lift the cover off. Use Figure D-1 to locate the various parts located below.

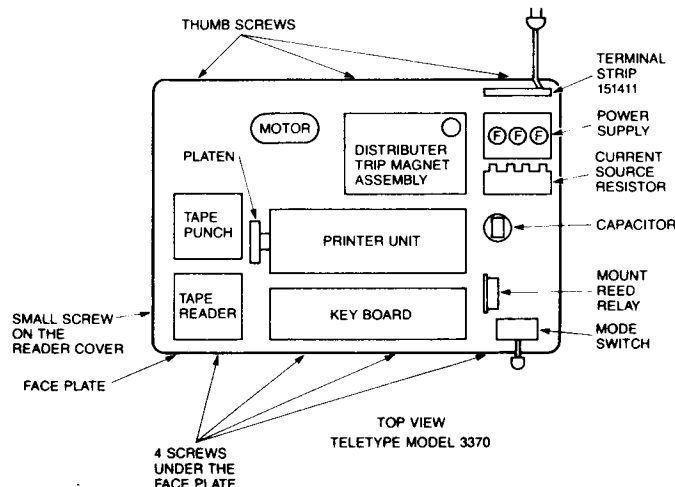


FIGURE D-1

The modifications are:

#### **CURRENT LOOPS CHANGED FROM 60 TO 20 MILLIAMPS**

The Current Source Resistor must be changed from 750 ohms to 1450 ohms. This is accomplished by moving the BLUE wire from Terminal #3 to Terminal #4 of the large power resistor shown in Figure D-2. The receiver current level is changed by moving the PURPLE wire of Terminal #8 on Terminal Strip 151411 to Terminal #9 on the same strip. Terminal Strip 151411 is shown in Figure D-3 with Terminal #1 at the far left.

#### **TELETYPE WIRED FOR FULL DUPLEX OPERATION**

The half duplex wiring must be changed by moving the BROWN/YELLOW wire from Terminal #3 to Terminal #5 and the WHITE/BLUE wire from Terminal #4 to Terminal #5 on Terminal Strip 151411.

#### **THE READER RUN RELAY ADDED**

The Reader circuit should have a 12 volt relay inserted to allow program control of the Reader. This Relay is shown along with the mode switch in Figure D-4. Mount the Relay with two 6-32 screws on the available bracket. A schematic diagram for the Relay and its connections is shown in Figure D-6. Locate the BROWN wire coming from the Distributor Trip Magnet which is connected to

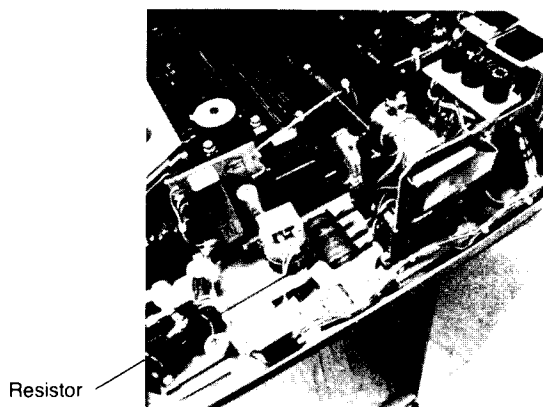
terminal J4—Pin 11 as shown in Figure D-5. Cut this BROWN wire and connect to the wire marked BROWN on the Relay circuit (note that this leaves J4—Pin 11 with no connection). Connect the wire marked LINE to terminal L1 and the wire marked LOCAL to terminal N of the mode switch as in Figure D-6. A preassembled Reader Relay Card is available from Intersil Inc., Model # 6908-RELAY.

#### **LEVEL 8 OPTION WIRED TO 'ALWAYS MARK'**

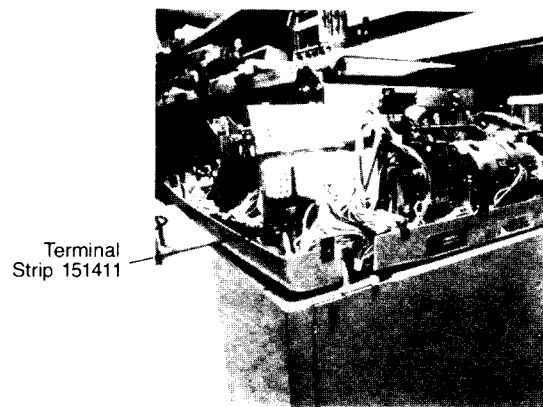
The level 8 option must be changed from parity to 'ALWAYS MARK'. This causes the keyboard to always output a 1 for the 8th bit, and the Reader to read the 8th bit as it was written. Locate the Left Contact Block and the Right Contact Block as shown in Figure D-7. It may be necessary to remove a clear plastic shield to gain access to the Left Contact Block. On the Left Contact Block remove the RED/GREEN wire from the upper left contact, leave the RED/GREEN wire open and connect the GREEN wire to the upper left contact. On the Right Contact Block connect the GREEN wire to the upper left contact. For a detailed reference see Teletype keyboard schematic 9334WD.

#### **CONNECT CPUTTY OUTPUTS TO THE TELETYPE**

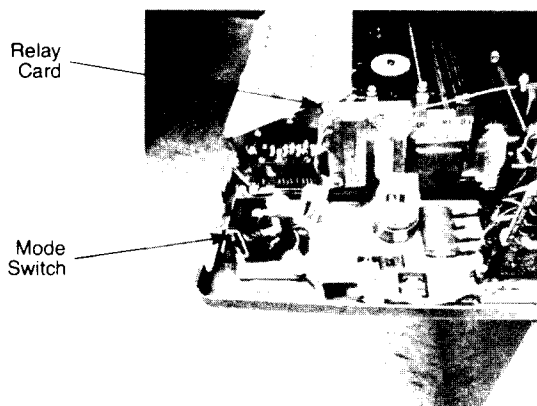
The TTY outputs of the CPUTTY board are connected to Terminal Strip 151411 and the relay as shown in Figures D-6 and D-8.



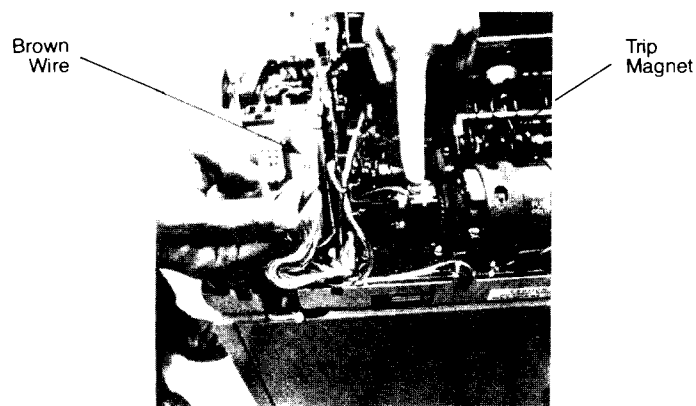
**FIGURE D-2**  
Current Loop Resistor



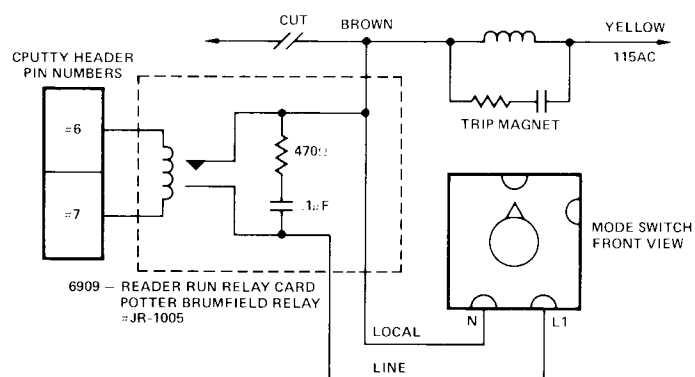
**FIGURE D-3**  
Terminal Strip



**FIGURE D-4**  
Relay Card



**FIGURE D-5**  
Distributor Trip Magnet



**FIGURE D-6**  
Reader Relay Circuit

# INTERSIL

10710 N. Tantau Ave., Cupertino, California 95014, (408) 996-5000, TWX 910-338-0228

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## 6801 SAMPLER KIT MANUAL

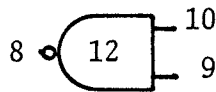
### -ERRATA-

The following changes will be required in order to use the new ROM  
ODT : IM6312-001 marked 63S003 ( in place of 63S000 ):

Figure 1 page 2: "SEL 4" (pin 9) should be shown to ground, not + 5V.  
"SEL 7" (pin 14) should be shown to + 5V, not ground.

Page 5: The first paragraph should read, "The PIE is wired to respond to the select code 00111 on SEL3 - SEL7 ...".

Page 6: The "pin-out" on the 74C00 should read as follows:



Page 33: OPTION 1, 'Note: Should read, "XTAL 11 and XTAL 21 should be used.".

Note: The description of the jumpers used, shown on page 33, is correct.